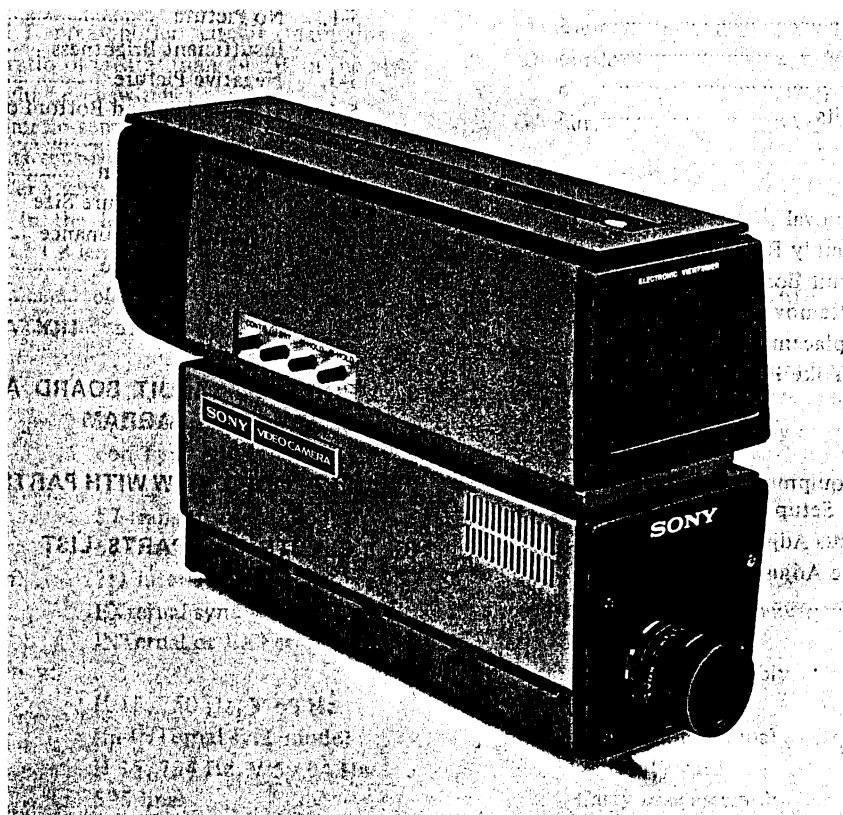


B/W VIDEO CAMERA

AVC-3250
3260



Original

SONY[®]
SERVICE MANUAL

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SECTION 1
GENERAL DESCRIPTION

1-1 INTRODUCTION

This manual provides service data for both the Model AVC-3250 and the Model AVC-3260 Video Cameras. These cameras are designed for use with the Sony videocorders and/or for use with closed circuit monitoring etc. Both cameras feature-high resolution (more than 550 lines)-light level switch (6dB sensitivity change)-external pedestal control.

The two camera models are similar except that the Model AVC-3260 also has an internal sync generator supplying both horizontal and vertical sync signals when the SYNC switch is switched to INT. mode. The FET preamplifier circuit maintains a signal-to-noise ratio of greater than 44 dB, in the high mode of the Light Level switch.

The automatic sensitivity control system automatically adjusts sensitivity to produce high resolution pictures over a very wide range of light levels (10 to 10,000 fc), in the low mode of the light level switch, (with an f 1.8 lens).

1-2 SPECIFICATION

Standard lens:	f 1.8, C mount
Camera tube:	2/3 inch separate mesh vidicon Type 8844 or equivalent
Semiconductors:	
AVC-3250	57 transistors, 28 diodes
AVC-3260	62 transistors, 28 diodes, 1 IC
Scanning system:	2:1 interlace
AVC-3250	EXternal sync
AVC-3260	INTernal or EXTernal sync
Scanning frequency:	
AVC-3250	H 15, 750 Hz; V 60 Hz (in INTernal sync mode)
AVC-3260	H 15, 734 Hz; V 59.94 Hz
Scanning lines:	525 lines
Frame rate:	30 Hz
Video output:	0.7v p-p, sync 0.3V, sync negative 75 ohms, unbalanced, UHF coaxial connector 6 pin connector
Horizontal Resolution:	
	High mode more than 550 lines at the center more than 400 lines at the corners
	Low mode more than 450 lines at the center more than 300 lines at the corners

Signal-to-noise ratio:	High mode 44 dB Low mode 40 dB
Minimum Illumination:	High mode 15 footcandles Low mode 1.5 footcandles
Automatic sensitivity range: (with f1.8 lens)	High mode 30-10,000 footcandles Low mode 10-10,000 footcandles
Power requirements:	117V AC, 60 Hz, 11 watts (27 watts with view finder)
Ambient temperature:	32°-104° F (0-40°C)
Dimensions:	4.33" x 4.75" x 13" (110(w) x 120(h) x 330(d) mm)
Weight:	AVC-3250 7 lb 13 oz (3.55 kg) AVC-3260 7 lb 15 oz (3.6 kg)

ACCESSORIES SUPPLIED:

AVC 3250S

Standard f1.8, 16mm lens
Camera cable
UHF cable assembly
Polishing cloth
Instruction manual
Warranty card

AVC 3250 DX

Zoom lens, VCL-1206
Camera cable
UHF cable assembly
Polishing cloth
Instruction manual
Warranty card
Carry case assembly
Elevator tripod
Microphone F-98
Mic Stand Assembly
Mic Strap Assembly
Mic Extension Cord
Lens Mount Cap Assembly
Lens pouch (case)
Viewfinder AVF-3250

AVC 3260S

Standard f1.8, 16mm lens
Camera cable
UHF cable assembly
Polishing cloth
Instruction manual
Warranty card

2-1 Features

Both the AVC-3250 and the AVC-3260 video cameras include the following features.

1. Light level switch — 2 position
 - High Light level
 - Low Light level
2. Pedestal level control
 - Permits external adjustment of pedestal level.
3. Increased sensitivity preamplifier
 - a. Provides higher resolution on strong signals (high contrast scenes)
 - b. Provides better signal-to-noise ratio under poor light level conditions.
4. Rugged focus mechanism
 - Provides increased mechanical strength and stability to withstand vibration, shock and temperature variations.
5. Electronic viewfinder AVF-3250
 - Easily attaches and plugs into the camera power supply.
6. The cameras accept any C mount lens.

The AVC-3260 video camera has the added feature of a built in sync generator to furnish both vertical and horizontal drive signals.

2-2 General

The AVC-3250 uses four printed circuit boards, the Process and Deflection (PD) board, the Preamplifier (PA) board, the Power Supply (PS) board and the Fuse (F) board. Refer to the disassembly section of this manual.

The vidicon is mounted inside a coil assembly directly behind the lens mount. This coil assembly contains the focus coil (outer) and the deflection coils (inner) all sharing a common axis.

2-3 VIDICON

The vidicon has a relatively long life and is a good low cost camera tube. The minor disadvantage of the vidicon is the image retention characteristic from an overly bright high contrast scene. The output current is approximately 0.2 μ A and is amplified by the preamplifier electronics.

In the Low Light Level Mode the vidicon target voltage is increased to increase sensitivity. This also increases the dark current about one and half times to approximately 200 nA.

The preamplifier gain is increased by two in the Low Light Level Mode. (High Mode 1 V p-p output

with 0.2 microamps input, Low Mode 1Vp-p output with 0.1 microamps input.) Video output from the vidicon is obtained from a spring connector that contacts the target ring at the lens end of the vidicon. This signal is routed directly to the preamplifier board. Beam intensity is adjusted by vidicon control bias. Spot focus is adjusted by focus coil current adjustment.

The target is made of a flat plate on which the optical image is focused by the lens. A photoconductive material is coated on the electron gun side of the faceplate to make the target. Contact is made to this coating by a metal ring built into the outer circumference and brought out through the glass.

The electron beam emitted by the cathode is controlled, accelerated, and focused by the voltages applied to the electrodes. The accelerating voltage is 300 volts. A mesh screen with 750 lines per inch between the target and the electron gun serves to provide a uniform electrostatic field to improve the uniformity of the beam landing on the target. The mesh has an applied voltage to form the electric field. The separate mesh type of vidicon features high uniform resolution and better response at the corners of the target. This type of vidicon also features very low picture distortion and negligible deterioration of resolution due to an excessive electron beam.

The target area is a uniformly deposited layer of photo conductive material. The electrical conductivity of the target is directly dependent on the intensity of light on the target, (more light-more conductivity).

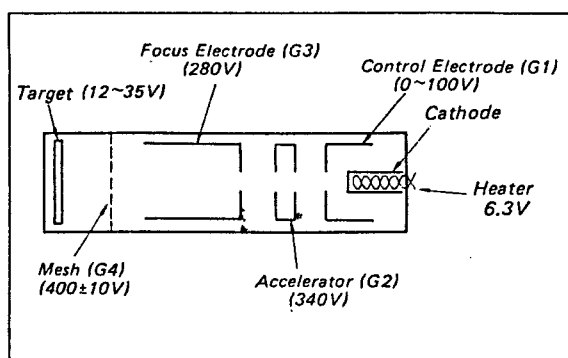


Fig. 2-1. Vidicon

2-4 VIDEO CIRCUITS

Automatic Sensitivity Control circuit (ASC)

The ASC circuit is used to maintain an approximately constant video output signal from the vidicon over a large range of light levels. Automatic Sensitivity

AVC 3260 DX

Zoom lens, VCL-1206
Camera cable
UHF cable assembly
Polishing cloth
Instruction manual
Warranty card
Carry case assembly
Elevator-tripod
Microphone F-98
Mic Stand Assembly
Mic Strap Assembly
Mic Extension Cord
Lens Mount Cap Assembly
Lens pouch (case)
Viewfinder AVF-3250

1-3 OPERATION

1. Precautions — to protect the vidicon:

- a. Avoid mechanical shock.
- b. Do not Carry the camera with the lens pointed downwards.
- c. Never point the camera at a source of intense light or at the sun. When outdoors be careful of camera placement.
- d. Avoid continuous shooting of a high contrast subject in strong light. Move the camera frequently to prevent the image "burning in" at the camera tube target. To eliminate minor target burns, turn off the camera and put the lens cap in place for an hour or so. As an alternative, direct the camera at an all white scene with uniform bright illumination of 100 footcandles for a few minutes.
- e. Keep the camera away from magnetic fields such as found near television sets, motors, transformers etc. The picture will become distorted and unstable from the influence of any external magnetic fields.
- f. When not in use turn off the camera, —keep the lens cap in place, keep the camera horizontal and well ventilated.
- g. Save the carton and packing material for use in change of location of the camera.

2. Lighting

Optimum object illumination for both model cameras is 60 footcandles. This illumination can be achieved with three 100 watts lamps. Ten footcandles is the minimum illumination in the high mode for these cameras. Both the video output and the

signal-to-noise ratio deteriorate when illumination is less than the specified minimum.

The low mode permits minimum illumination level of 1.5 footcandles.

3. Operation

Signal connections

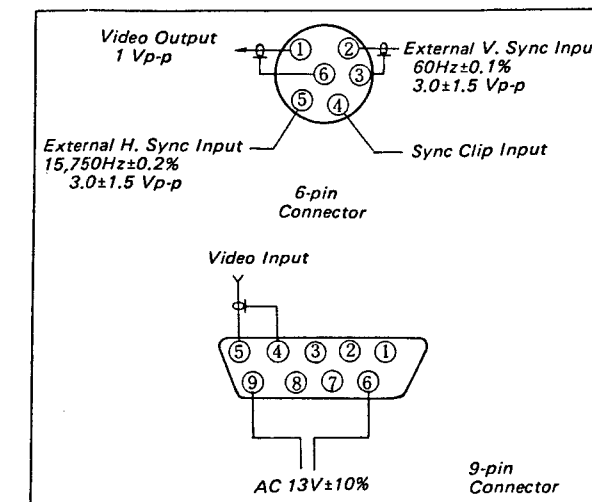


Fig. 1-1. Signal connections

Operation with a Videocorder

Connect the camera to the Videocorder using the camera cable with the 6 pin connectors, (cable CCF). To operate from a separate sync generator, Sony Model CG-1, use the EXT position of the sync selector and connect the camera to the sync generator using the camera cable with the 6 pin connectors (cable CCF). Connect the Videocorder to the camera via the UHF coaxial cable.

Alternately the videocorder can receive the video from the sync generator unit by way of the CCF connection to the camera.

When using the AVC-3250 camera with Internal sync the vertical sync is locked to the power line frequency and the horizontal sync is from the free running oscillator.

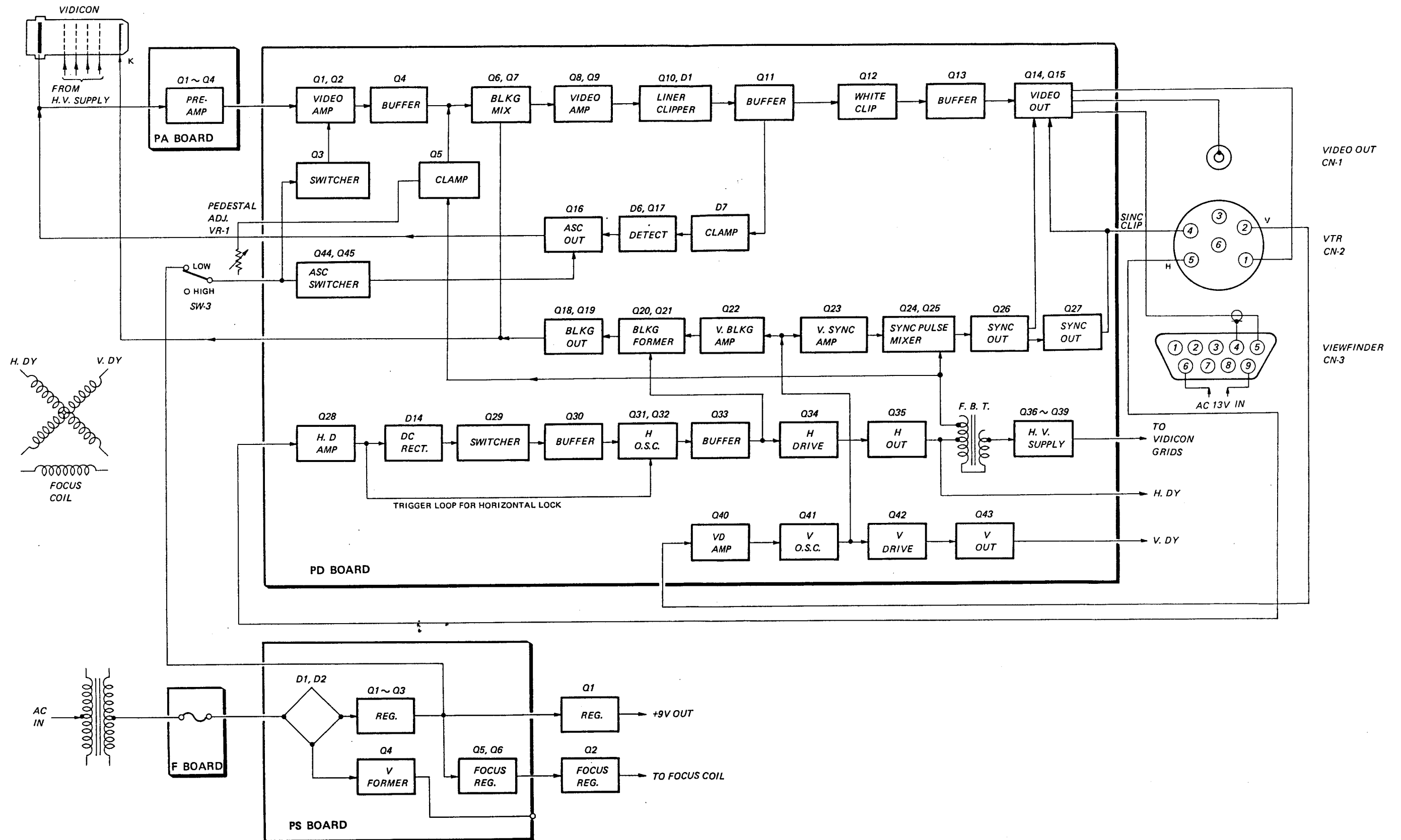
When using the AVC-3260 camera however, with INTERNAL sync both the horizontal and vertical sync signals are provided by the INTERNAL sync generator circuitry.

Operation with a video monitor or conventional TV receivers: Either the AVC-3250 or the AVC-3260 camera requires a separate RF modulator to generate a channel 3 (or other channel) TV signal to operate directly into the antenna input terminals of a conventional TV receiver or receiver/monitor. When using this RF connection arrangement the camera is operated in The INTERNAL sync Mode.

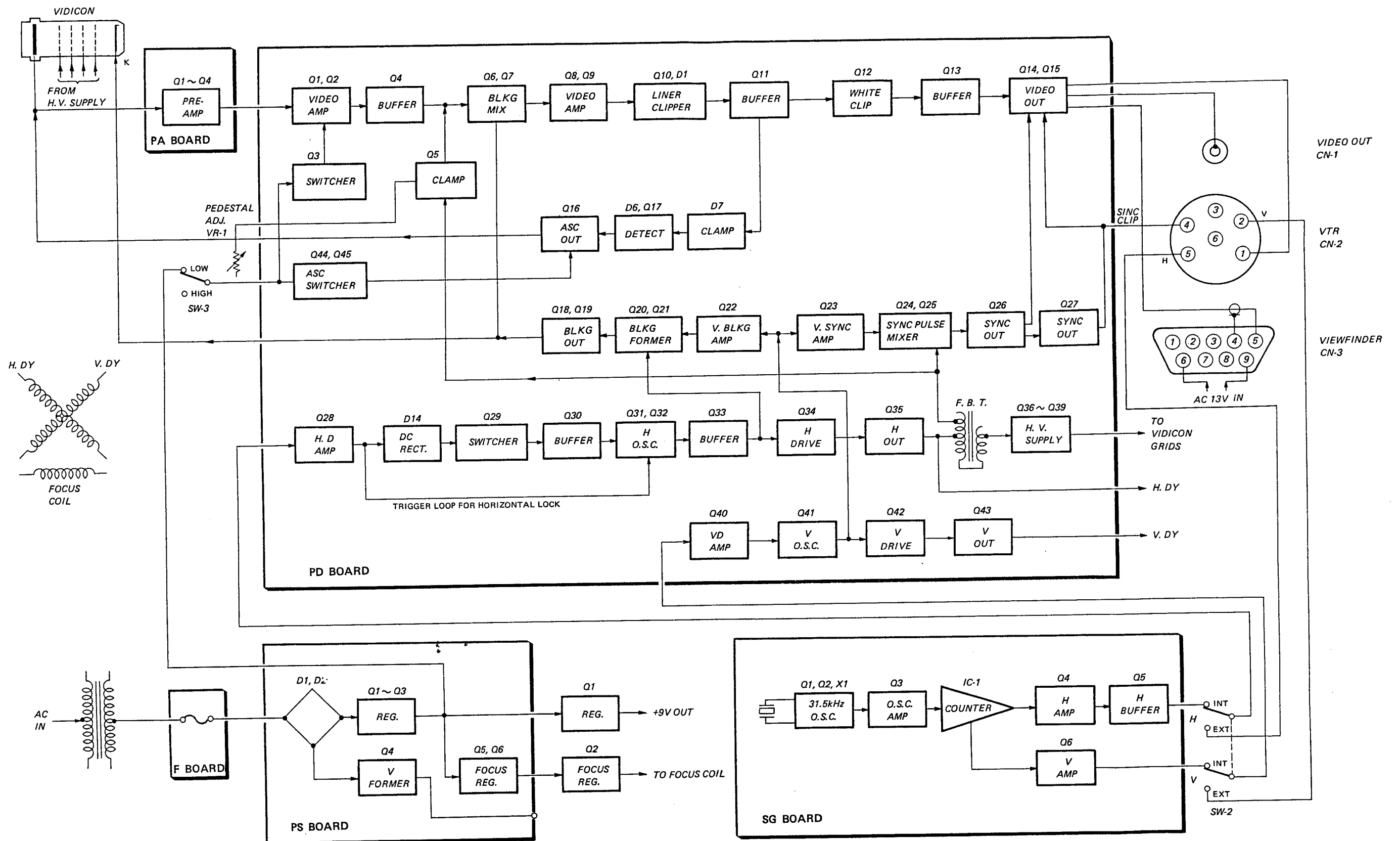
SECTION 2

CIRCUIT DESCRIPTION

BLOCK DIAGRAM: AVC-3250



BLOCK DIAGRAM: AVC-3260



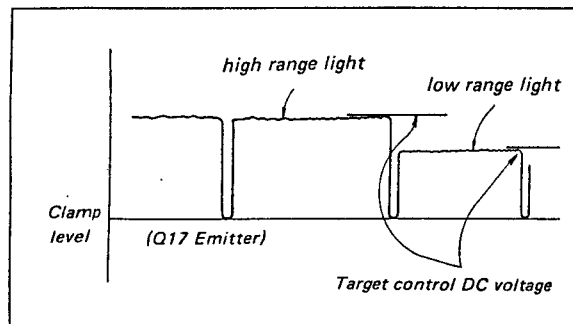


Fig. 2-2. ASC Target clamp level

The inductance resonates with the high distributed capacitance of the vidicon target and output circuit in series with the preamplifier input capacitance. The Percival circuit most importantly isolates the vidicon output capacitance from the preamplifier input capacitance. This resonating and isolating effects an improved impedance match and improved signal transfer particularly for the higher video frequencies. The resistance lowers the resonant circuit Q and this limits the amplitude of the peaking.

The Preamplifier (PA) board.

The input signal enters the preamplifier through the Percival circuit. The three stage, directly coupled preamplifier of Q1, Q2, and Q3 incorporates an overall negative feedback loop which controls the gain of the upper video frequencies for purposes of compensation. The individual amplifier stages each employ an emitter peaking circuit to boost the high frequencies. Since the vidicon output signal has a very high signal-to-noise ratio, the effective signal-to-noise ratio of the FET preamplifier input stage (Q1) determines the practical signal-to-noise ratio of the camera.

The fourth transistor Q4 is an emitter follower to supply the preamplified video signal at low impedance to the video amplifier on the Process and Deflection board.

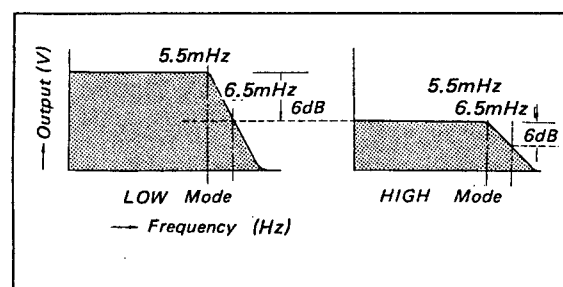


Fig. 2-4. Light level switch

The Process and Deflection board.

The input circuit of the video amplifier is made up of a two transistor complimentary coupled amplifier with a third transistor connected as a switch. The switching changes the filter components in the amplifier from a flat characteristic series filter to a parallel filter with frequencies below 3.5 mHz boosted by 6 dB. When the third transistor Q3 is switched off the filter components made up of two resistors, two capacitors and two inductors are all connected in series and provide a flat frequency response.

The switching transistor Q3, when switched on,

grounds the midpoint of the filter string and the currents flow in the two filter paths in parallel, (each branch includes R, L, and C). Frequencies below 3.5 mHz are boosted and frequencies above 3.5 mHz conversely are attenuated, so that a 6 dB ratio exists between the two frequency ranges.

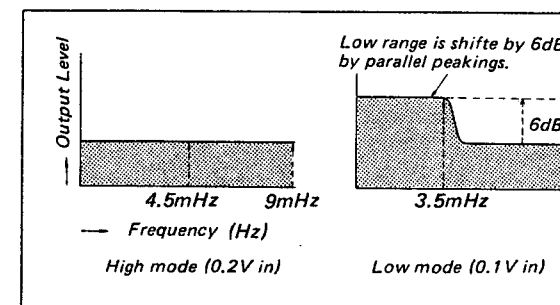


Fig. 2-5. Overall characteristic of Video Amplifier

The vidicon target voltage is also changed by the Light Level switch. The Light Level switch, in the Low mode, turns on transistor Q44 which in turn switches Q45 off. Q45 is in parallel with R160, thus the current through the voltage divider VR-6 and R160 decreases. The voltage on the wiper arm of VR-6 rises and thus the collector of Q16 also rises, which increases the vidicon target voltage. The increased target voltages raises the dark current output of the vidicon in the Low Light mode, as well as the vidicon sensitivity. See Fig. 2-3 and 2-4. Also as evidenced in Fig 2-5, the low frequency gain of the video amplifier, (below 3.5 mHz) is also boosted in the Low Light mode.

The output of the complimentary coupled and switched video filter stage Q1, Q2 is impedance changed in emitter follower Q4 to a low impedance signal and supplied to the Q6 input of the pair of summing amplifiers Q6 and Q7. The signal path between Q4 and Q6 is shunted by the externally adjustable pedestal clamp, Q5. Amplifiers Q6 and Q7 share a common collector load impedance. The input to Q6 is the clamped video signal. The input to Q7 is the blanking signal. The output is the combined video and blanking signal.

The combined signal is amplified in Q8 and Q9, clipped in Q10 and buffered in Q11. The video output level is adjusted by control VR-2 in the emitter circuit of Q11. The signal is white level clipped by Q12. The clipped video signal and the sync signal are summed in Q13. Q13, Q14 and Q15 provide the signal gain at low impedance to perform the power amplification to drive the output cables and the viewfinder. The buffered signal level at Q11 is also sampled and supplied to the Automatic Sensitivity

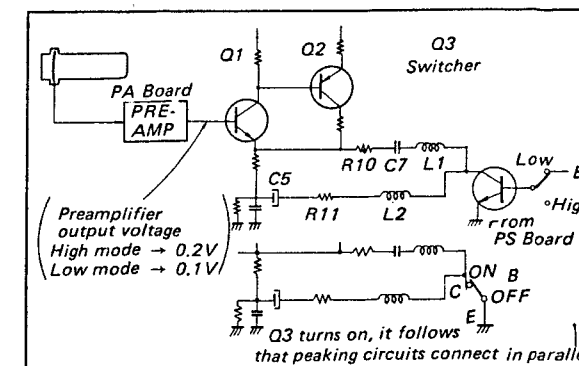


Fig. 2-6. Switcher operation

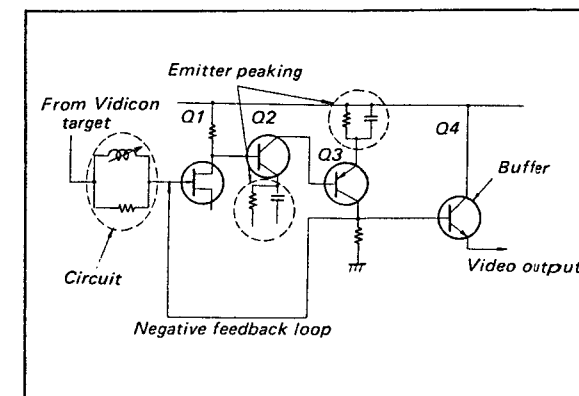


Fig. 2-7. PA board

Control is achieved by sampling the amplified video signal from video amplifier Q 11 output, (before the sync component is added) rectifying the video sample in diode D6 and amplifying in a DC amplifier, Q 16. The output of the amplifier Q 16 is shunted across the voltage source of the vidicon target load resistor. Thus as the video signal is increased due to increased illumination of the subject, the larger video signal provides a larger DC voltage which in turn causes a heavier load on the vidicon target source voltage.

The vidicon target voltage is thereby decreased and the camera sensitivity is reduced. When scene illumination lowers, the decrease in video signal feeds back a smaller signal and the shunt loading effect of Q 16 is reduced effectively raising the target voltage and raising the camera sensitivity. This feedback arrangement is self regulating and provides nearly constant output over a wide range of lighting conditions.

The vidicon output is preemphasized by a Percival circuit. The Percival circuit is a parallel network of inductance and resistance inserted in series with the input signal applied to the gate of the first stage FET, of the preamplifier.

Control circuit, which is described in the initial paragraphs of paragraph 2-4, and shown in part in Fig 2-3.

The vertical sync and the horizontal sync are summed into a combined sync signal in the circuits of Q23, Q24, Q25, and Q26, Q27. Transistor Q23 is configured as a Miller Integrator and shapes the vertical oscillator signal prior to supplying the vertical signal to the input of Q24. Transistor Q24 and Q25 form a summing amplifier with a common output. The input to Q25 is the horizontal sync pulse.

The two sync signals are combined in the common collector load resistance which is directly coupled into the base input of Q26 configured as a PNP emitter follower. The output of emitter follower Q26 is diode coupled to the base of emitter follower Q27. The base of emitter follower Q27 is also connected through a diode to pin 4 of the six pin connector. Thus if pin 4 of the six pin connector is grounded the input base of the emitter follower Q27 is grounded and Q27 has no output. The diode coupling between Q26 and Q27 permits Q27 to be grounded without an effect on Q26. The emitter resistor of Q27 terminates into a video signal bus inputting in to Q14 of the (PD) circuit board. The output of Q14 therefore is composite (video com-

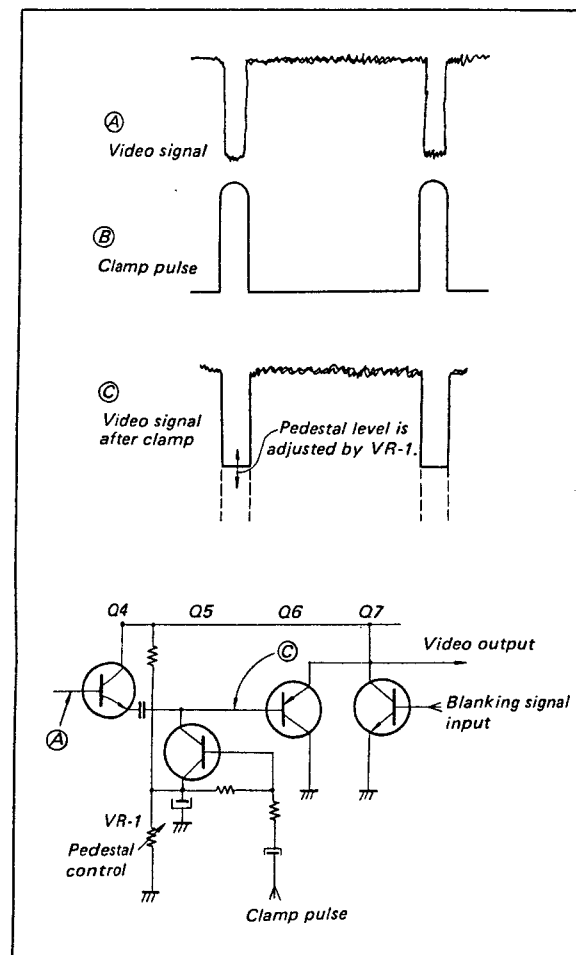


Fig. 2-8 Pedestal clamp

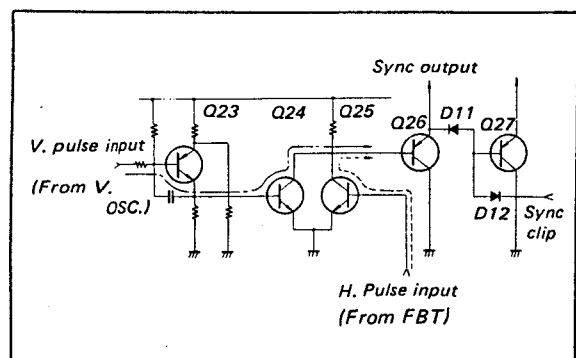


Fig. 2-9. Sync mixer

bined with sync) when pin 4 is not ground and non composite, (video only) when pin 4 is at ground. The output of Q14 is connected to pin 1 of the six conductor cable. Therefore remote grounding of pin 4 controls the composite or noncomposite video on line 1.

The blanking signal for both video blanking and vidicon blanking is generated in the group of transis-

tors Q18 through Q22.

Vertical oscillator output is supplied to Q22 where the signal is integrated and shaped. The output of Q22 is supplied to one input of the summing pair of Q20 and Q21. The other input receives the horizontal pulse signal. The common output has the combined vertical and horizontal waveforms and is amplified at low impedance by Q18 and Q19. The resultant signal is supplied as the video blanking and vidicon blanking signal.

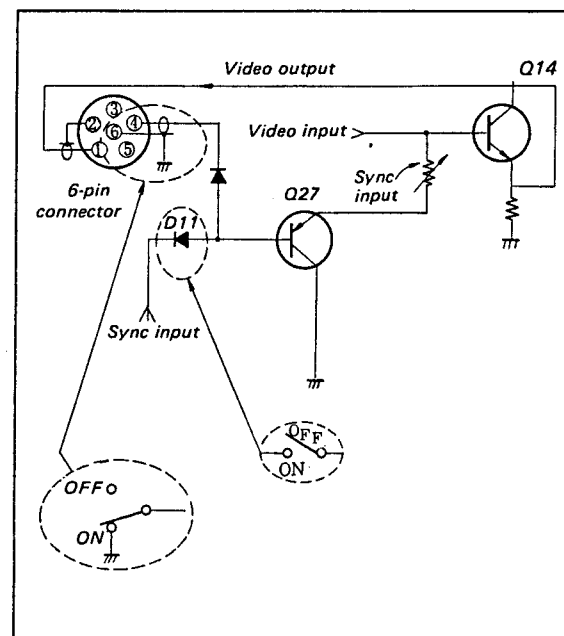


Fig. 2-10. Sync clip

Horizontal Deflection Circuit.

The horizontal deflection circuit operation is affected by the setting of the Sync switch. With external sync the Model AVC-3260 camera circuit detects the presence of sync. The sync is rectified and DC amplified. The DC signal raises the bias of the horizontal oscillator so as to increase the period of oscillation (lowers the horizontal oscillator free-running frequency). The sync signal is turn however triggers the oscillator to cause the oscillator to be synchronized with the external sync generator.

When the external sync signal is absent the horizontal oscillator bias is lowered and the oscillator free runs at a higher frequency.

The horizontal oscillator signal is buffered in Q33 and supplied to a power amplifier Q34 and Q35, to provide the signal driving capability. The output drives the deflection coil of the vidicon and the fly back transformer. The high voltage of the flyback

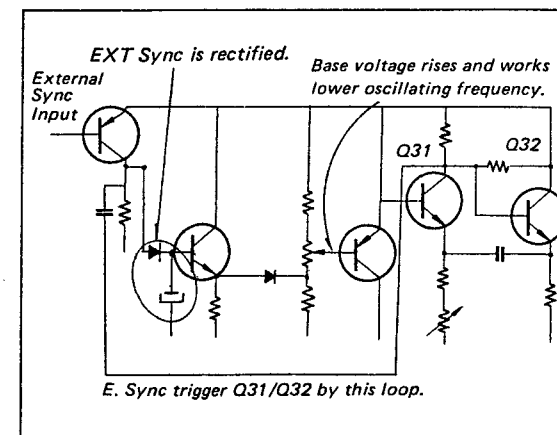


Fig. 2-11. Horizontal oscillator

circuit is regulated and divided in the transistor string Q36 through Q39 and supplied to the proper vidicon electrodes.

Vertical Deflection Circuit.

A blocking oscillator is used for the vertical oscillator. The vertical sync signal (either external or internal) is applied to the base of Q40 for amplification and pulse shaping and supplied to trigger the blocking oscillator, Q41. The vertical oscillation is amplified in the Q43, Q44 power amplifier and supplied to the vertical deflection coils. The deflection coil reactance is sufficiently low so that the vertical voltage waveform is nearly sawtooth to provide a sawtooth current and resultant sawtooth shaped magnetic field and deflection.

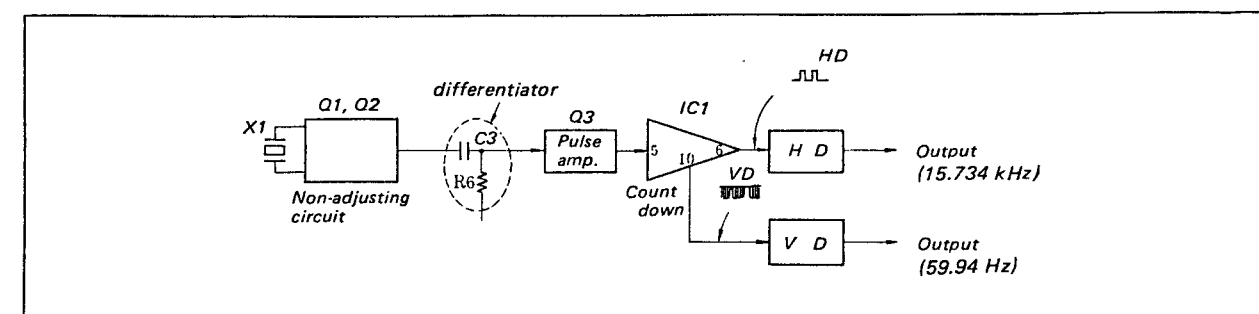


Fig. 2-12. SG Board Block Diagram

Sync Generator (SG) board (AVC-3260 only)

The sync signals are both generated from a single crystal oscillator. The crystal frequency is twice the horizontal frequency, and 525 times the vertical frequency. This relationship of the horizontal and vertical frequencies is necessary for the 2 to 1 interlace requirement of the camera specification. The frequency division is accomplished in the sync generator by the integrated circuit MN115.

The crystal oscillator is made up of the transistor circuits Q1 and Q2. The 31.468 kHz signal is differentiated and amplified in transistor Q3 and supplied to the dividing IC. The vertical drive signal is applied to the base of Q6 and the horizontal drive signal is applied to the base of Q4. Q5 is the horizontal output amplifier.

Power Supply (PS) Board

The power supply furnishes regulated output voltage using transistors Q1 through Q3.

The series regulator transistor Q1 is connected in a Darlington circuit with a separately mounted transistor (also Q1) on a chassis heat sink.

A second constant current regulator supplies the current to the focus coil from transistors Q5 and Q6 on the PS board and also transistor Q2 on the separate heat sink mount on the chassis. Constant current is supplied to the focus coil even though the resistance of the focus coil changes with temperature.

SECTION 3

DISASSEMBLY

3-1 Cabinet Removal

1. Stand the camera in its normal position as shown in Fig. 3-1.
2. Remove the top panel by taking out the two screws at the top of the camera.
3. Turn the camera upside down as shown in Fig. 3-1.
4. Remove the side panels by taking out the four screws.

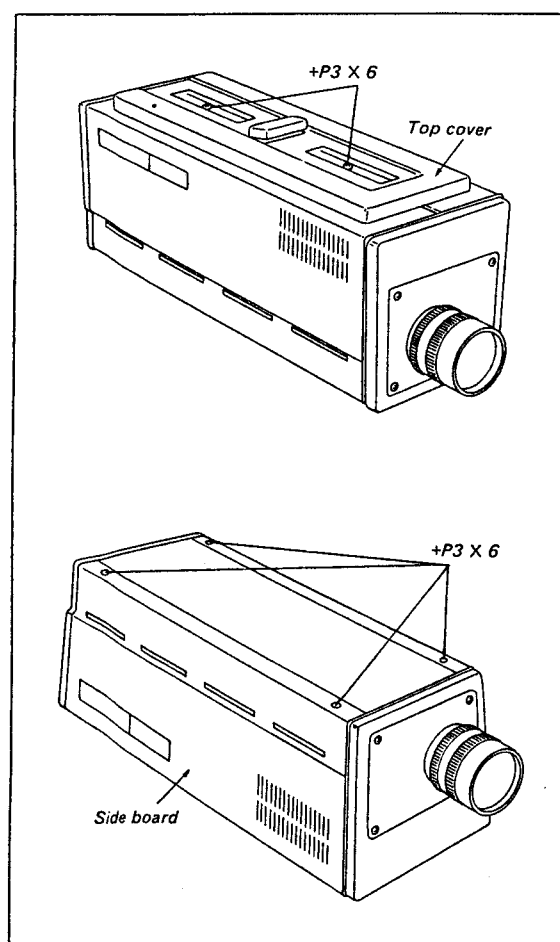


Fig. 3-1. Cabinet removal

3-2 Frame Assembly Replacement

When the frame assemblies are damaged, replace them as follows. See Fig. 3-2 and 3-3.

1. Secure the escutcheon panel to the front panel assembly with the four screws.

2. Guide the five bosses of the front panel assembly into the holes of the chassis.
3. Mushroom the tips of the bosses with a soldering iron.
4. Use the same procedure to assemble the rear panel.

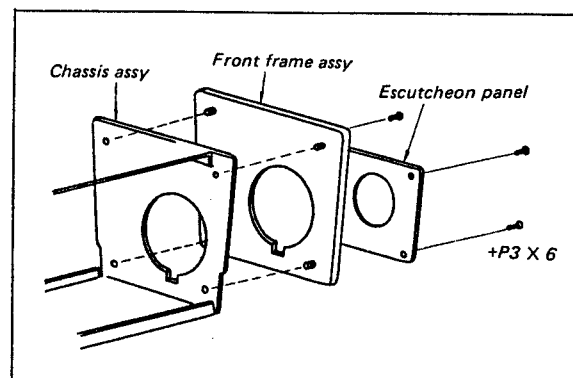


Fig. 3-2. Front frame removal

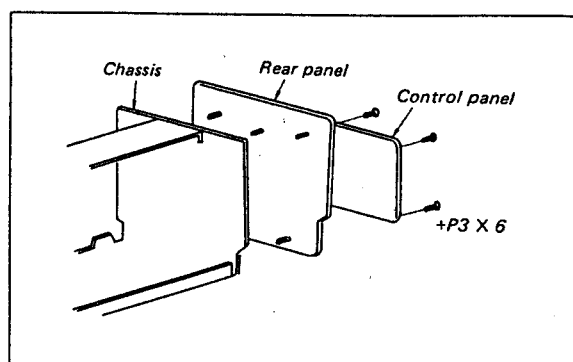


Fig. 3-3. Rear frame removal

3-3 PRINTED CIRCUIT BOARD REMOVAL

3-3-1 Process and Deflection (PD) board removal.

Remove the case

Remove the two screws on the metal bracket attached to the PD board as shown in fig. 3-4.

Remove the four SV terminals connected between the PD board and the Preamplifier board.

Remove the PD board by pulling the board out of the mating connector.

SV connections starting from the outer corner of the PD circuit board: Yellow

Red

Black

White/orange

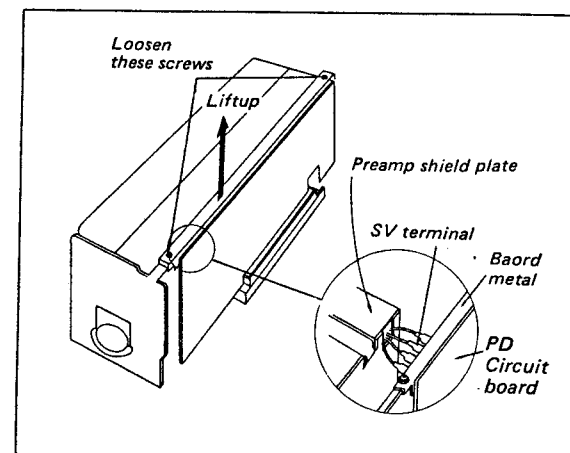


Fig. 3-4. PD circuit board removal

3-3-2 Preamplifier (PA) board removal.

Unscrew and remove the lens.

Remove the four screws retaining the front panel escutcheon. Remove the four flat head screws retaining the lens mounting plate. Note the plastic flanged insert in the back of the lens mounting plate, as the lens mounting plate is removed. On the front of the camera unsolder the target lead from the terminal just above the vidicon. The PA board is completely shielded. Remove the two screws retaining the PA assembly as shown in fig. 3-5. The PA board can be removed by passing the SV connections disconnected from the PA board, through the clearance hole in the PA shield case. To completely disconnect the PA board, the ground terminal to the vidicon shield must be unsoldered. The PA board is reinstalled in the reverse order.

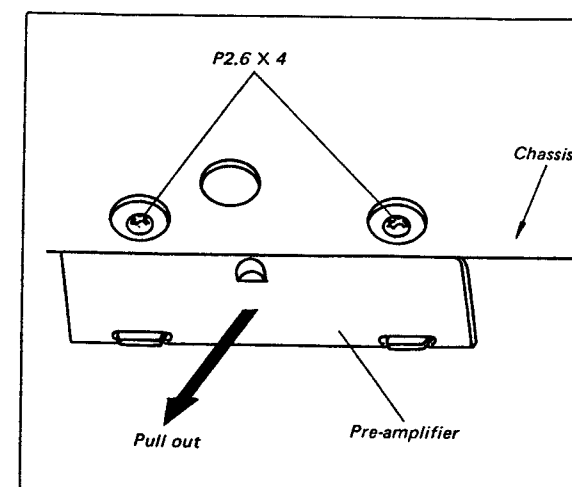


Fig. 3-5. PA circuit board removal

3-3-3 Power supply (PS) board removal.

Remove the two screws retaining the PS board bracket. Push the board out of the camera just enough so that the two screws holding the bracket to the circuit board can be removed allowing removal of the bracket.

The PS Circuit board can then be pushed through and out of the camera assembly on the opposite side. The PS board is not electrically disconnected and can be used for testing while supported on its leads.

3-3-4 Fuse (F) board removal.

Remove the single screw retaining the F board, remove the fuse board.

3-3-5 Sync Generator (SG) board removal.

On model AVC-3260 cameras the SG board is mounted in a shielded enclosure next to the PA board enclosure. Slide the circuit board out of the enclosure.

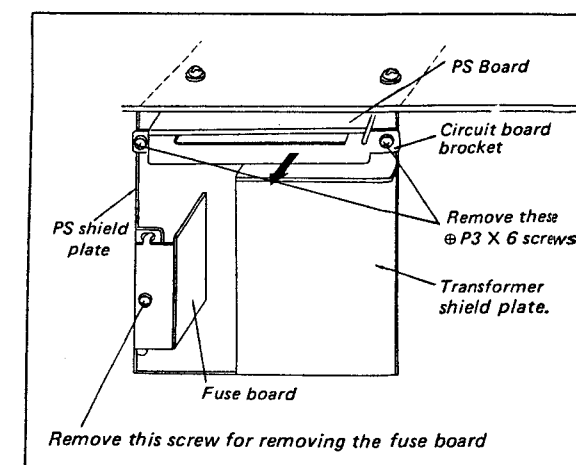


Fig. 3-6. PS circuit board removal

3-4 CONNECTOR REMOVAL

To remove the rear panel connectors and the pilot lamp from the connector panel, proceed as follows. See Fig. 3-3.

1. Remove the four screws from the connector panel.
2. Remove the connector panel.
3. Remove the PD board: refer to paragraph 3-2.
4. Unsolder the leads connected to the connectors.
5. Remove the nut(s) from the connector(s).

SECTION 4 ALIGNMENT

6. Remove and replace the connector(s) and or the pilot lamp.
7. Reassemble the panel in the reverse order.

3-5 VIDICON REPLACEMENT

1. Remove the four screws from the escutcheon panel (P2.6x6).
2. Loosen the four screws from the lens mount.
3. Disassemble the vidicon holder, the vidicon spring, the vidicon socket etc.
4. Remove the vidicon from the deflection yoke.
5. Replace the vidicon properly seating it in the socket.
6. Reassemble the vidicon spring and the vidicon holder.
7. Reassemble the lens mounting plate with the flanged plastic collar inserted into the back of the mounting plate. The mounting plate must be pressed against the vidicon which is spring loaded against the lens mounting plate. Be sure to locate the slot, in the lens mounting plate, downward, so as to permit access with the focus adjustment jig (or screwdriver if the jig is

- not available).
8. Complete the procedures of section 4-6-9 Centering, Size and Linearity. Section 4-6-10 Magnet Alignment. Section 4-7 Video Alignment.

3-6 DEFLECTION YOKE REMOVAL

1. Remove the vidicon as in section 3-5.
2. Unsolder the deflection coil leads from the terminals on the internal connector for the PD board. Make note of lead locations and color.
3. Place the camera upside down.
4. Remove the deflection yoke assembly from the chassis by removing the four retaining screws.
5. Disassemble the yoke assembly and reassemble with the replacement yoke.
6. Replace the deflection yoke assembly in the chassis and solder the cable in place.
7. Replace the vidicon and lens assembly.
8. Complete the alignment procedures of sections 4-6-9 Centering, Size, and Linearity; 4-6-10 Magnet Alignment 4-7 Video Alignment.

4.1 Required Equipment

Sync Generator	Sony CG-1
Tripod	Sony VCT-20A
Video Monitor	Sony CVM or PVM Monitor
Lens	Sony Standard Lens Supplied
Zoom Lens	Sony VCL-1206; or VCL-16B
Cables	UHF Coaxial Cable 6 conductor CCF cable
Test pattern	180 mm X 240 mm
Fluorescent lamp or	Equivalent
Volt-ohmmeter	Simpson 260 or equal
VTVM	HP 3300A with 3305A plug-in or equal
Oscilloscope	Tektronix 561, or 422 or equal.

4-2 PRELIMINARY SETUP

1. Connect the camera and monitor with the UHF coaxial cable. Connect the sync generator to the camera with the 6 conductor plug and cable. (The model AVC 3260 can use INTERNAL sync in lieu of the separate sync generator.)
2. Uniformly illuminate the test pattern with about 60 footcandles, (600 LUX). Two 20 watt fluorescent lamps about a meter (39") from the pattern will be satisfactory. The ASC circuit operates best above light levels greater than 60 footcandles.
3. The distance between the test pattern and the lens mounting ring must be precise, to properly fill the vidicon target. The distance can be determined by the test pattern height and the focal length of the lens using the following equation;

$$\text{Subject distance (mm)} = \frac{F.1. (h + 6.3)}{6.3} - \frac{F.1. (h + 6.3)}{h} - 17.526$$

F.1 lens focal length in mm
h test pattern height in mm

In the case where the height of the test pattern is 180 mm and the lens focal length is 16 mm the correct distance is 439 mm. (17 1/4")

4. Install the standard lens on the camera.
5. Align the center of the lens and the center of the test pattern, with the camera perpendicular

to the test pattern.

6. Rotate the lens barrel for optimum focus. Read the indicated focus distance on the lens barrel scale. The indicated focus should be approximately correct. If not correct and the camera is not to be used with a zoom lens, adjust the mechanical position of the vidicon as in step 7.
7. To adjust the vidicon position first adjust the lens barrel to the proper focus setting on the scale. Insert the alignment jig as shown in fig 4-3. Turn the jig clockwise or counterclockwise for optimum focus. (This adjustment is also used to make the zoom lens track properly and so is prescribed below when using the zoom lens.
8. Switch the monitor to underscan. Play back the Monoscope portion of the VTR Alignment tape to adjust the monitor size and linearity.

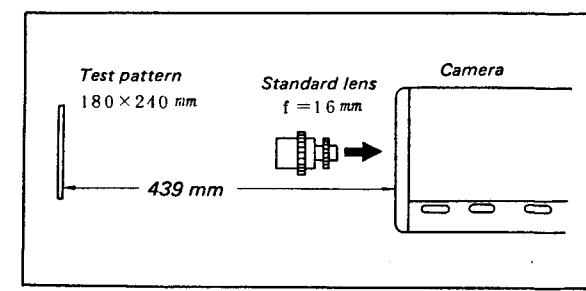


Fig. 4-1. Preliminary Set-up

4-3 ZOOM LENS TRACKING ADJUSTMENT

1. Remove the lens from the lens mount. Remove the escutcheon.

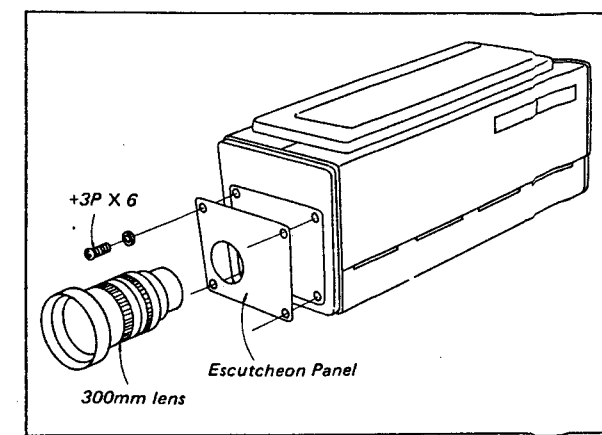


Fig. 4-2. Optical Focus Adjustment

- 2. Place the viewfinder on the camera. Connect the video monitor by means of the UHF cable.
- 3. Install the zoom lens. Point the camera at an object approximate 10 meters away (30 ft.). Rotate the zoom, to the telephoto position. Focus the image by adjusting the focus ring on the lens.
- 4. Without readjusting the focus ring on the lens verify correct focus at the wideangle position of the zoom adjust. If not at optimum focus then perform the following step 5.
- 5. Insert the alignment jig shown in Fig. 4-3 and turn the jig clockwise or counterclockwise for optimum focus.

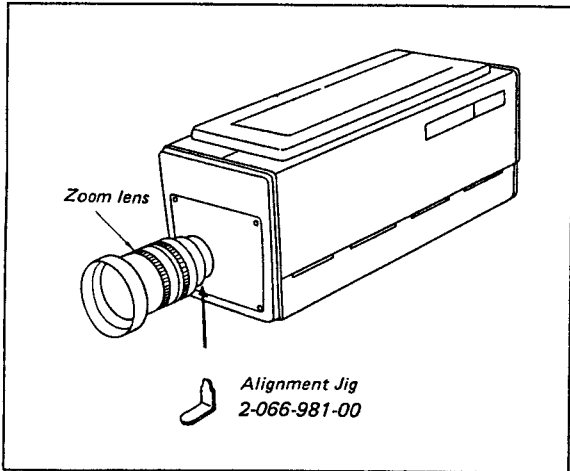
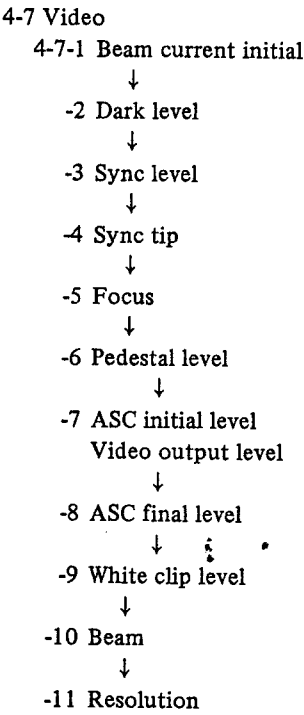
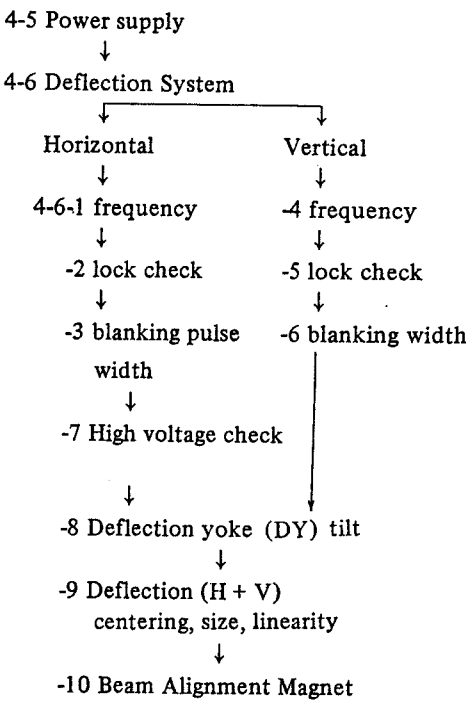


Fig. 4-3. Optical Focus Adjustment

- 6. Recheck the focus at both ends of the zoom adjust (telephoto and wideangle). Confirm correct focus throughout the zoom range. If the alignment jig is not available a small 2mm screwdriver can be used with extra care. The vidicon can be damaged if the screwdriver is inserted too far.

4-4 STEPS OF THE ADJUSTMENT PROCEDURE

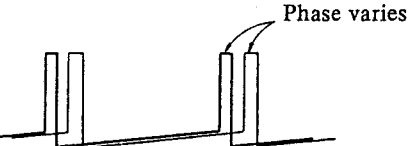
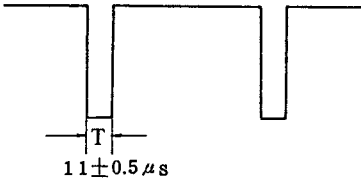
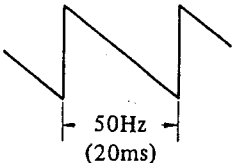


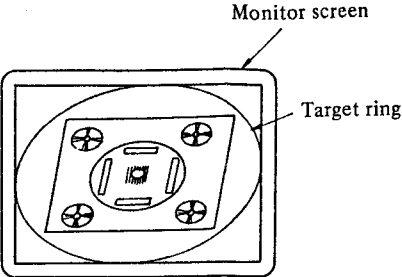
4-5 POWER SUPPLY

Adjustment	Test point/Board	Procedure and Value	Control	Object	Equipment Required
4-5-1 9V Supply	PD Q2 Collector	Value: $9 \pm 0.1V$ 	VR-1		VOM

4-6 DEFLECTION SYSTEM

4-6-1 Horizontal Frequency (Free-running)	TP-4/PD	1. Set VR-8 to midrange. Adjust VR-16 for pulse width of $T=10.5 \pm 0.2 \mu s$. 2. Readjust VR-8 for $63.5 \mu s$ period. ($15,750 \pm 10 \text{ Hz}$) Note: To obtain free running Horizontal oscillator:— on AVC 3250-set to INT sync on AVC 3260-set to EXT sync without connecting an external sync generator 	VR-16 (H. Pulse Width) VR-8 (H. Freq)		Oscilloscope (Counter)
4-6-2 Horizontal Lock Check	TP-4/PD	AVC3250 Connect sync generator, CG-1 to the 6 pin connector without power to the sync generator, Switch sync generator power ON-OFF.			Oscilloscope

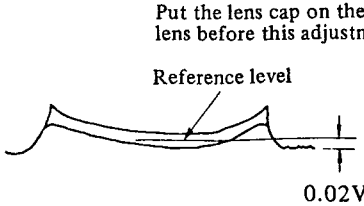
Adjustment	Test Point/Board	Procedure and Value	Control	Object	Equipment Required
		AVC-3260 Without a sync generator connected switch sync selector between INT, and EXT. Lock will be evidenced in both cases when phase changes when changing above switch positions. <div>  </div>			
4-6-3 Horizontal blanking pulse width	PD Q-18 emitter	Verify pulse width at Q18; emitter of T=11±0.5μs. <div>  </div>			Oscilloscope
4-6-4 Vertical Frequency (free-running)	TP-1/PD	AVC-3250 and AVC-3260-Sync switch to external-sync generator not connected. Adjust VR-12 for 50 Hz (or 20 ms period). <div>  </div>			Counter (Oscilloscope)
4-6-5 Vertical Lock check	TP-7/PD	AVC 3250 Sync switch to external, sync generator connected to 6 pin connector. Switch sync generator power ON and OFF. AVC 3260 sync generator not connected Switch Sync selector from EXT-INT. Confirm that the vertical locks to 60 Hz with a period of 16.7 ms with			Oscilloscope

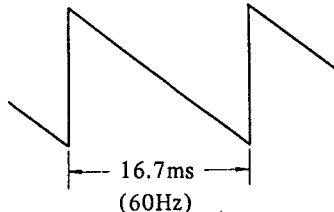
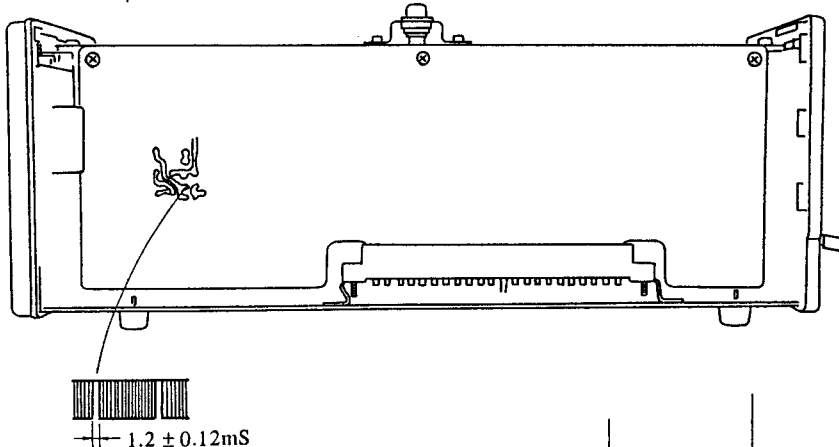
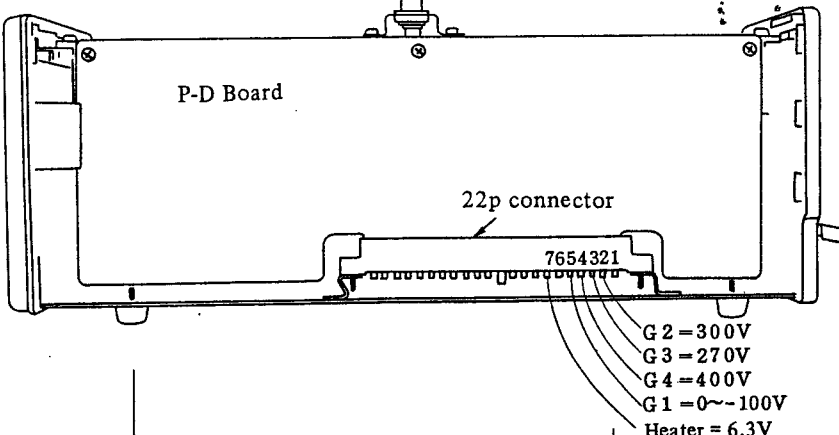
Adjustment	Test Point/Board	Procedure and Value	Control	Object	Equipment Required
Size, Linearity	Monitor	<div>  </div> Adjust VR-13 Vertical Linearity, L-5 width, and VR-14 height, to full the screen with the test pattern. Use underscan on the monitor.	VR-9 (H.Cent) VR-14 (Height) VR-13 (V.Line) VR-14 (Height) L-5 (H.Width)		Test Pattern
4-6-10 Beam Alignment Magnet	Monitor	Adjust the beam alignment magnet, so that shading, flicker, ripple, electrode reflection, and picture distortion are minimum. Readjust centering (4-6-9) Confirm resolution at corners of 400 line minimum.	Alignment Magnet		Test Pattern

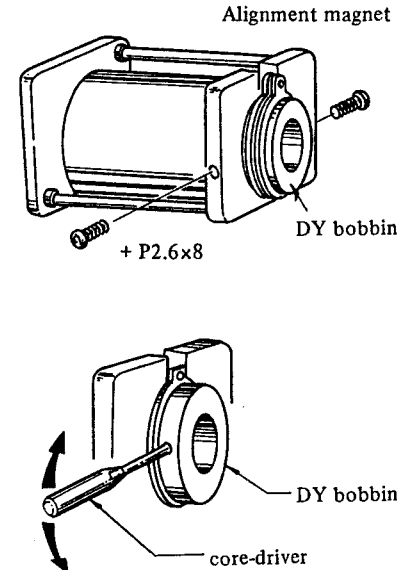
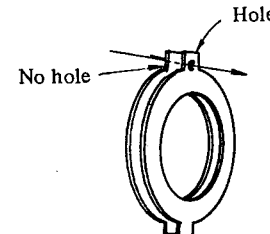
4-7 VIDEO SYSTEM ALIGNMENT

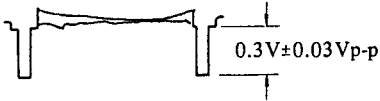
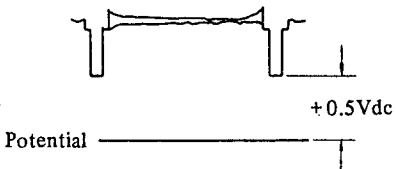
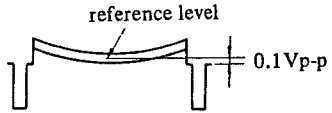

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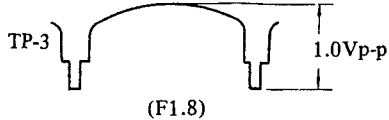
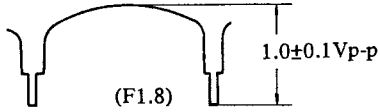

It is necessary to perform the complete video adjustment at one time, (section 4-7-1 to 4-7-11). Do not perform a partial alignment, because interaction of the circuitry will degrade performance.

4-7-1 Beam current tentative adjustment	Monitor screen	Set VR10 (Beam) initially so that a picture can be observed on the monitor screen.	VR-10/PD (Beam)	Standard operating condition	Monitor
4-7-2 Dark level	TP-1/PD	LOW mode <div>  </div> Put the lens cap on the lens before this adjustment.	VR-6/PD (Dark)	None required	Oscilloscope

Adjustment	Test Point/ Board	Procedure and Value	Control	Object	Equipment Required
		either sync generator switched on, (or Sync switched to INT.) 			
4-6-6 Vertical Blanking Pulse Width	Q18/PD	On emitter of Q18 confirm pulse width of $T=1.2\text{ms}\pm0.12\text{ms}$ 			Oscilloscope
4-6-7 High Voltage check	TP-6/PD		VR-11		VOM (VTVM)

	Test Point/ Board	Procedure and Value	Control	Object	Equipment Required
4-6-8 Deflection Yoke Tilt	Monitor	Loosen the two screws P2.6x8 which hold the deflection yoke as shown below. Insert the core driver into the DY hole. Rotate the def- lection yoke to eliminate tilt. Replace screws. 	DY bobbin		Monitor
4-6-9 H+V Centering, Size, Linearity		Set the distance, the lens, and the test pattern as indicated in paragraph 4-2 (steps 3, 4, and 5). (Preliminary set up) Set the alignment magnets as shown below 			Illuminated test pattern
Centering	Monitor	Increase deflection by adjusting VR-13. Vertical linearity and L-5 Horizontal Width, Adjust VR-15 Vertical center- ing so that vidicon target ring is visible on all four corners.	VR-13 (V.Line) L-5 (H.Width)	Standard operating condition	Monitor

Adjustment	Test Point/ Board	Procedure and Value	Control	Object	Equipment required
4-7-3 Sync level	TP-2/PD TP-3/PD	Terminate output with 75 ohms 	VR-4/PD VR-5/PD (Sync level)	None Required	Oscilloscope
4-7-4 Sync Tip Voltage	TP-2/PD TP-3/PD	Terminate output terminal with 75 ohms  Potential After this adjustment, confirm dark level adjustment, Section 4-7-2	VR-1/PD (Clamp)	None required	Oscilloscope
4-7-5 Focus	Monitor screen	Adjust VR-2 (Focus) for the best possible focus of the test pattern on the monitor screen.	VR-2/PS (Focus)	Standard operating condition	Monitor
4-7-6 Pedestal level	TP-3/PD	Place the lens cap in place and adjust VR-1 (Pedestal) under the connector panel. (HIGH mode) 	VR-1 (Pedestal)	None required.	Oscilloscope
4-7-7 ASC initial and video output level adjust- ment	TP-1/PD	(HIGH mode) A. Set the iris of the lens for F1.8, then adjust VR-7 for 0.2Vp-p at TP-1. 	VR-7/PD	Standard operating condition	Oscilloscope
	TP-3/PD	(2) Adjust VR-2 (video level) for 1.0Vp-p video signal at TP-3/ PD (video out)	VR-2/PD (video)		

Adjustment	Test Point/ Board	Procedure and Value	Control	Object	Required equipment
		 Recheck the Pedestal level (section 4-7-6) and Sync Clip level (section 4-7-4) after this adjustment			
4-7-8 ASC final level	TP-3/PD	A. Set the iris of the lens to F1.8, then adjust the video level to obtain 1.0±0.1Vp-p at TP-3. B. Verify that the signal at TP-1 is 0.2Vp-p. C. Check the video level when chang- ing the iris as follows F2.8 } 0.9±0.1Vp-p F4 } 	VR-7/PD (ASC)	Standard operating condition	Oscilloscope
4-7-9 White Clip	TP-3/PD	Set the iris of the lens to F1.8, then pan the camera at a high contrast object. Make the adjustment rapidly as the vidicon can be burnt. 	VR-3/PD (White clip)	Normal picture	Oscilloscope
4-7-10 Beam	TP-1/PD	A. Fully open the iris of the lens. B. Pan the camera at a low contrast object. C. Adjust VR-10 for 0.4Vp-p.	VR-10/PD (Beam)	Normal Picture	Oscilloscope
4-7-11 Resolution Check	Monitor	Confirm that resolution in each mode satisfies the specification value. High mode 550 lines at the center 400 lines at the corners Low mode 450 lines at the center 300 lines at the corners		Standard operating condition	Monitor Resolution chart

SECTION 5

TROUBLESHOOTING GUIDE

General

The following checklist is suggested should difficulty be experienced. Specific areas of the camera and system that might be the cause are suggested for troubleshooting.

1. No Picture
normal raster- no raster
2. Insufficient brightness
3. Reversed picture
4. Dark at top and bottom of picture
5. Poor resolution
6. Insufficient picture size
7. Insufficient luminance
8. Flicker in picture
9. Picture not stable
10. Noise in picture

5-1 NO PICTURE

Normal Raster

A. Check video system

1. Touch preamplifier input for noise on raster.
2. If noise is not evidenced, troubleshoot preamplifier. Refer to section 4-6-7.
3. If noise is in evidence

B. Check vidicon

1. Target ring spring connection?
2. Deflection coil?
3. Vidicon voltages?
4. If voltages are not correct, troubleshoot the High voltage section, including the flyback, transformer.
5. If voltages are correct, replace the vidicon.

No Raster

A. Check power supply Section

1. Fuse?
2. Power switch?
3. Diode rectifiers?
4. Regulator series transistor?
5. Power transformer?
6. Vidicon heater?
7. Other

B. Check Deflection System

1. Horizontal output?
2. Horizontal oscillator?
3. Vertical output?
4. Vertical oscillator?
5. Deflection yoke?

5-2 INSUFFICIENT BRIGHTNESS

Check Video System

1. TP-3 for 2V p-p
2. If not correct verify correct transistor supply voltage
If TP-3 voltage is correct
3. Pedestal clamp level?
4. Target voltage? Refer to 4-7-2
5. Video output level? Refer to 4-7-7 Adjust VR-2
6. Beam setting? Refer to 4-7-10 Adjust VR-10
7. High voltage?
8. Vidicon electrode voltage? Refer to 4-6-7
9. Other?

5-3 NEGATIVE PICTURE

Check video system

1. Vidicon target voltage? Refer to 4-7-2 Adjust VR-6.
2. Beam level? Refer to 4-7-10 Readjust Beam.
3. White Clip Level? Refer to 4-7-9 Readjust White Clip Level.
4. Other?

5-4 DARK AT TOP AND BOTTOM OF PICTURE.

Check Deflection system

1. Waveform at TP7? Refer to 4-6-5 Adjust VR-13.
2. Blanking pulse width?

5-5 POOR RESOLUTION

Check focus system

1. Power supply? Adjust VR-21/PS.
2. Vidicon faceplate for cleanliness?

Check video system

1. Target ring contact?
2. Contacts on 22P connector?
3. Excessive beam level? Adjust VR-10.
4. Optical focus? Adjust.

5-6 INSUFFICIENT PICTURE SIZE

Check Deflection system

1. Centering? Adjust VR-8, VR-15.
2. Waveforms at TP-5 and TP-7? Adjust L5+VR14.
3. Deflection yoke assembly?

5-7 INSUFFICIENT LUMINANCE

Check video system

1. Illumination of subject? — Increase illumination.
2. Lens cleanliness?
3. Vidicon faceplate?
4. Shading more than subject? Adjust alignment magnet.
5. Target voltage? Adjust VR-6
6. Video output level?
7. Replace vidicon?

5-8 FLICKER IN PICTURE

High voltage system

1. High voltage ripple?

Video system

1. Alignment magnet adjustment? Readjust centering.

Sync system

1. Verify H+V frequencies? Adjust VR-8, VR-12
2. Pedestal clamp?

5-9 PICTURE NOT STABLE

High voltage system

1. Ripple on vidicon electrodes?
2. Ripple on focus current?
3. Ripple on low voltage supply?
4. Monitor?

5-10 NOISE IN PICTURE

System connection

1. PCB connector?
2. Target ring connector?
3. Cold solder joints?

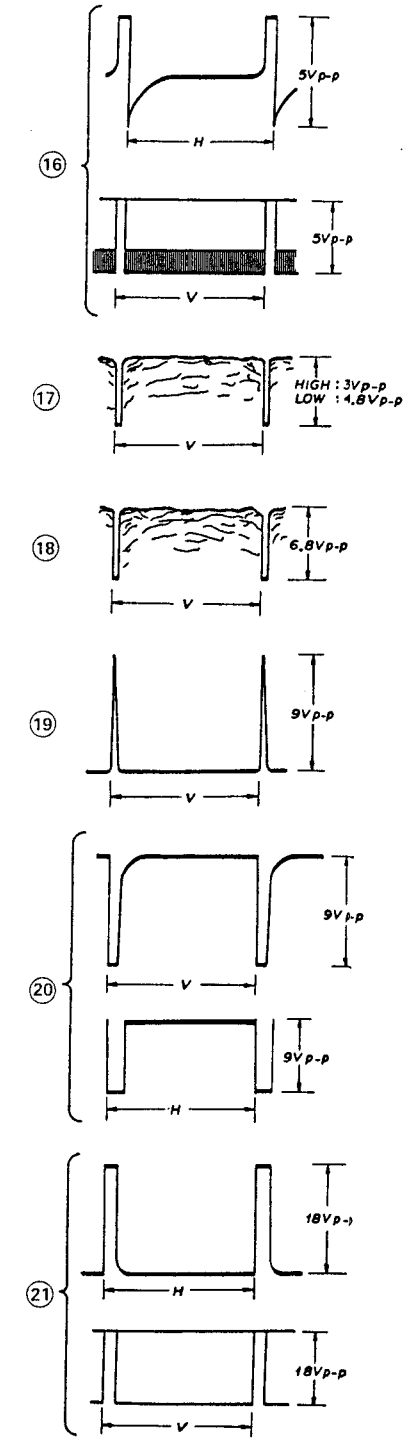
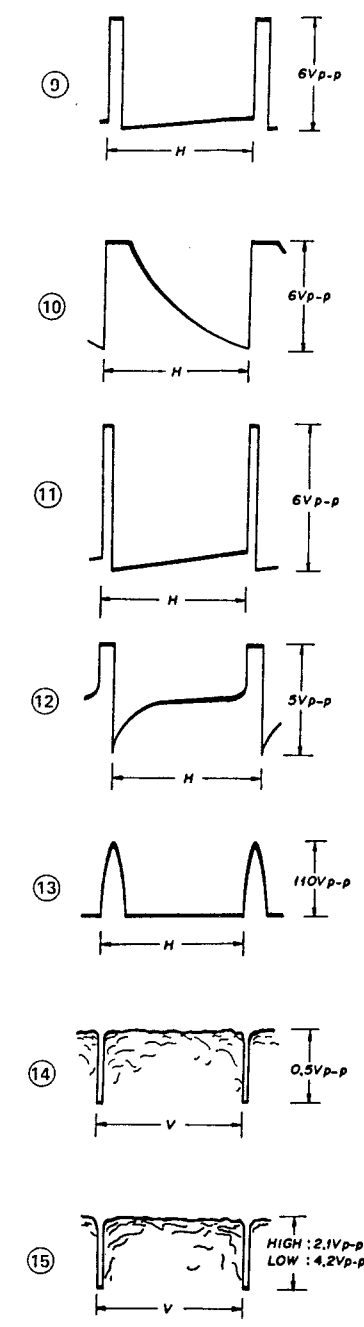
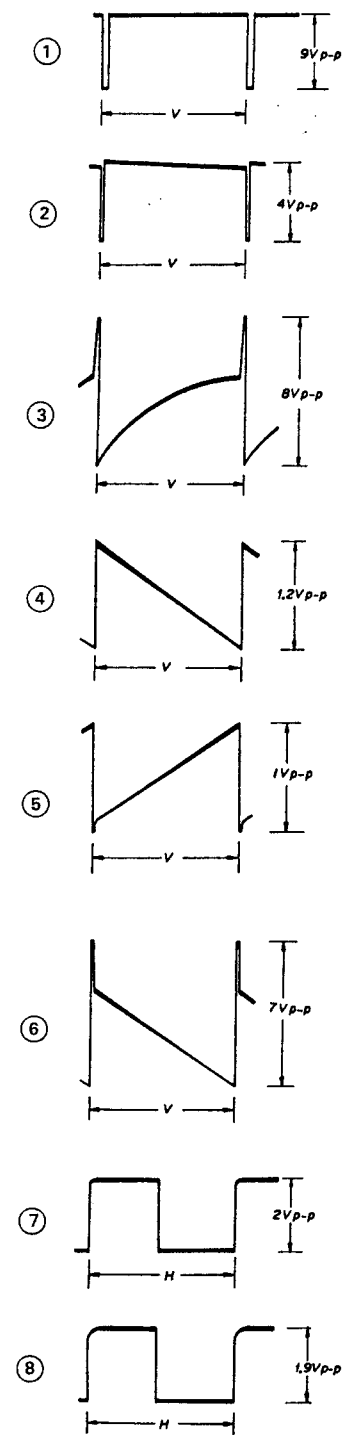
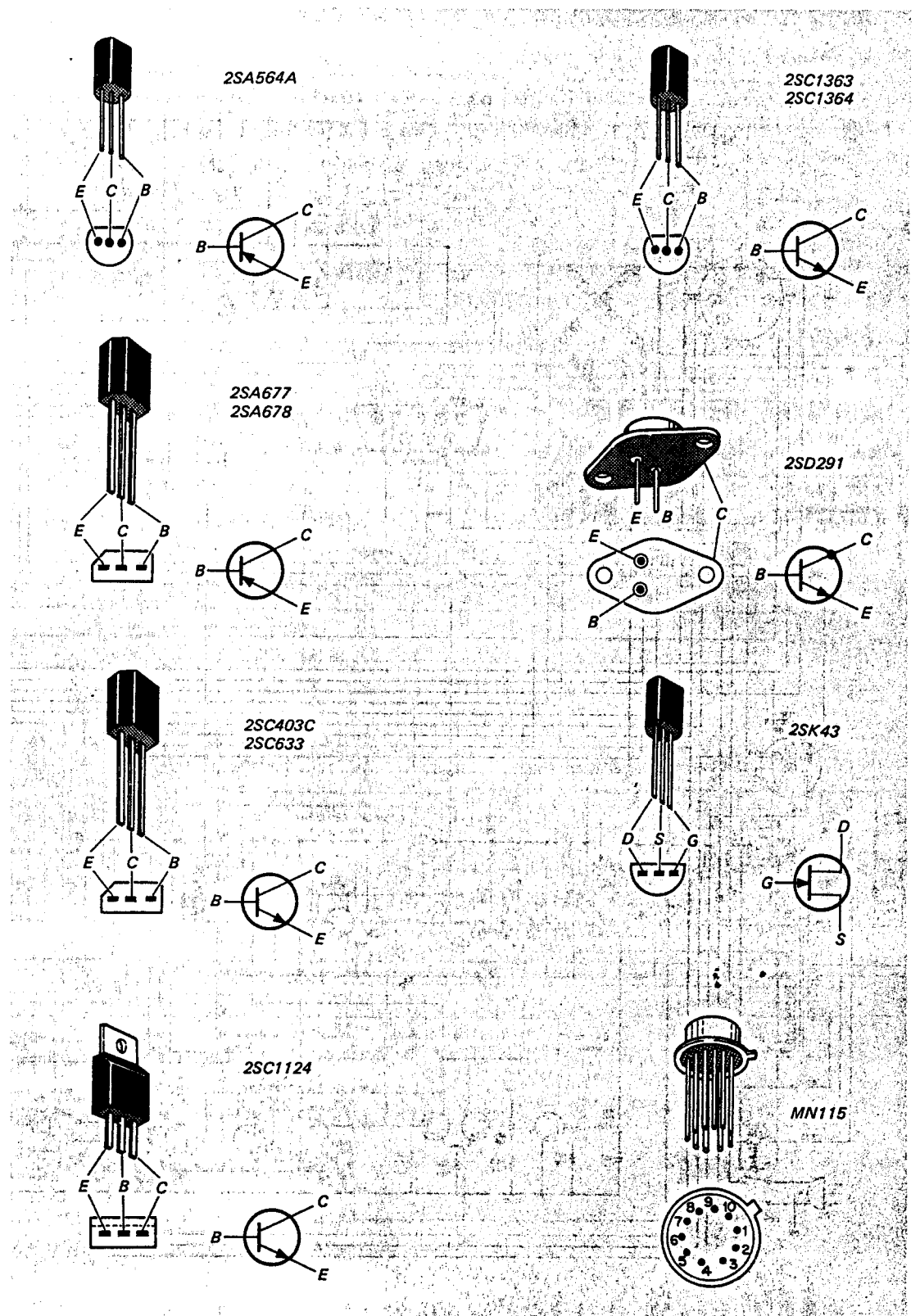
Video system

1. Target voltage?
2. Low light intensity?
3. Video amplifier oscillating?
4. External radio interference?

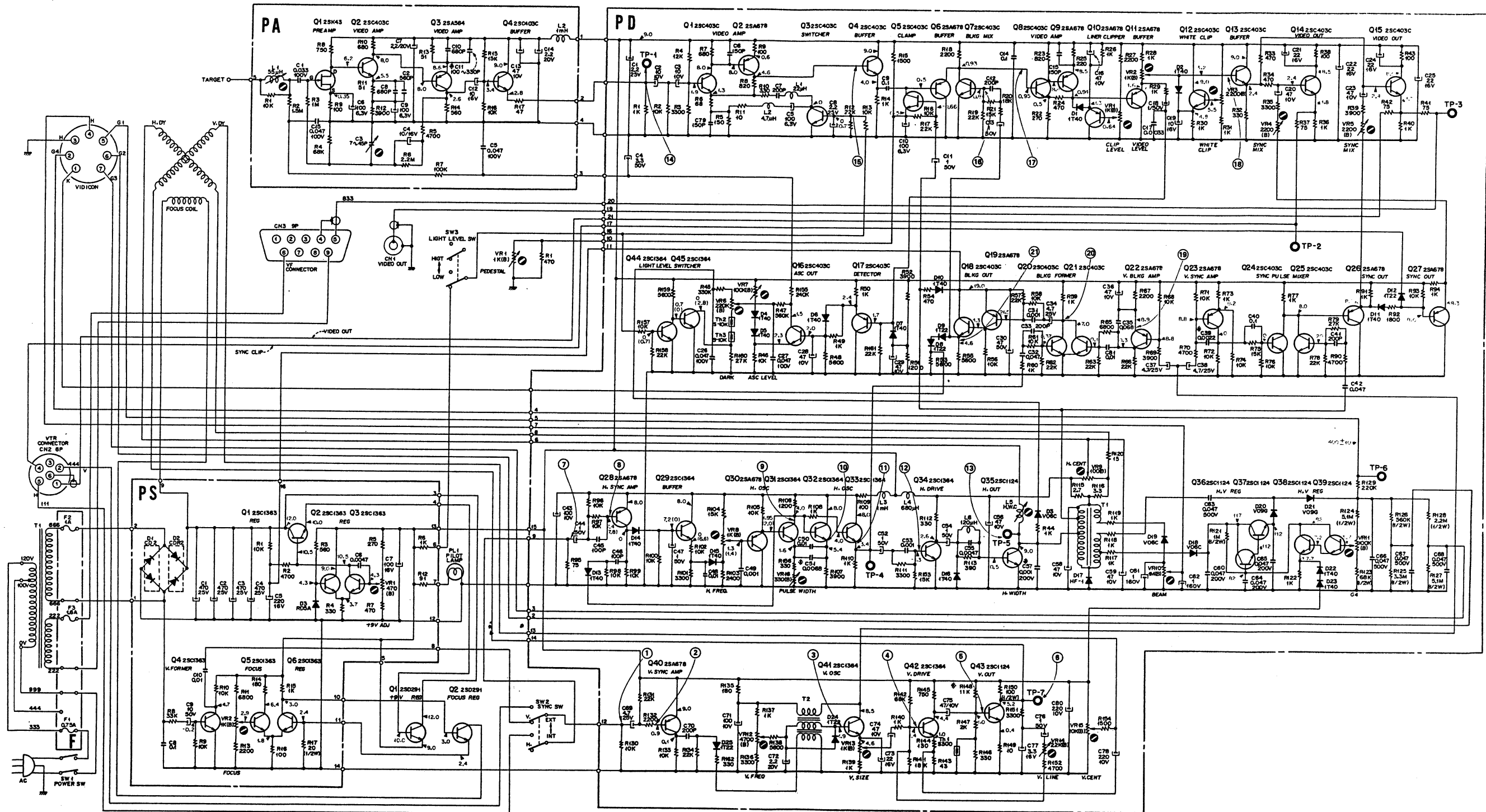
SECTION 6

PRINTED CIRCUIT BOARD AND SCHEMATIC DIAGRAM

SEMICONDUCTOR ELECTRODES



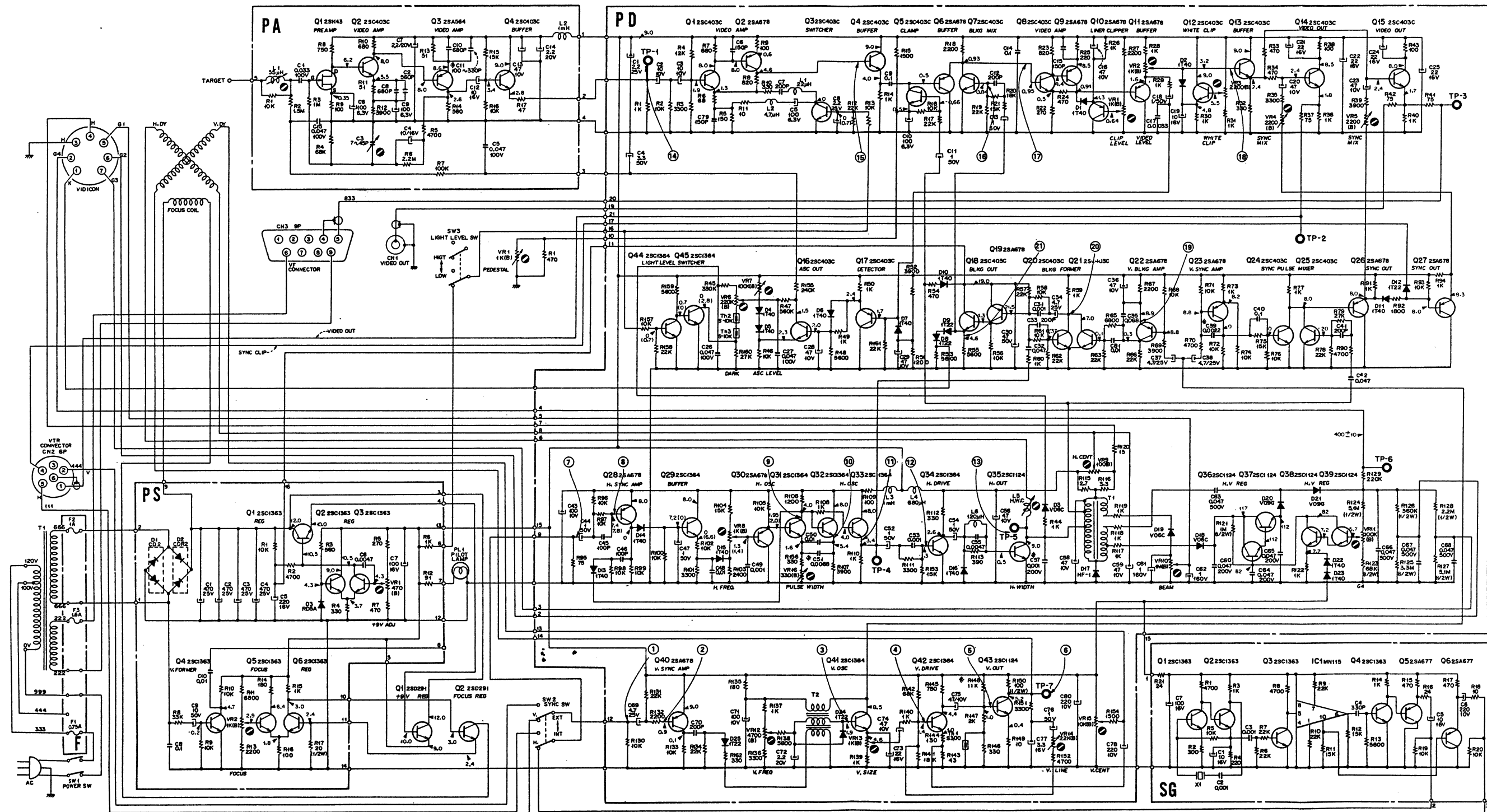
AVC-3250 SCHEMATIC DIAGRAM

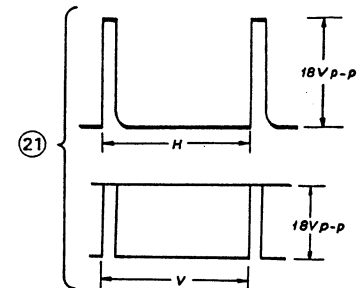
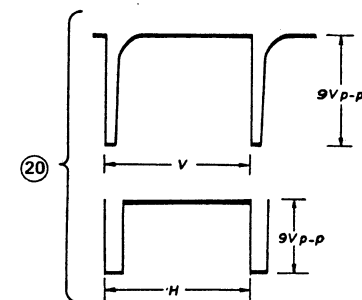
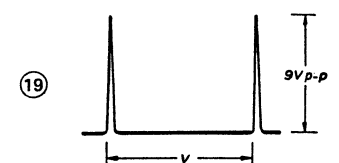
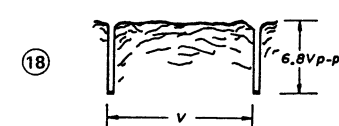
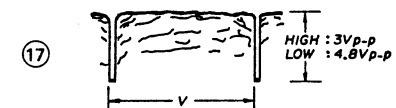
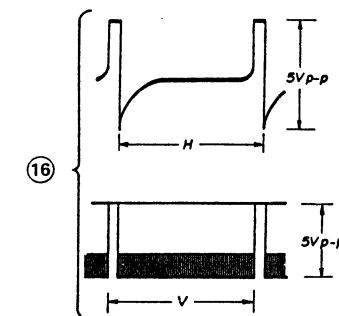
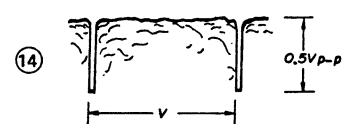
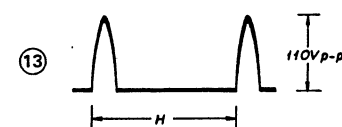
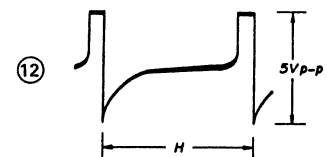
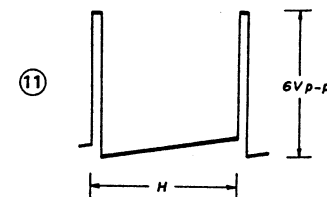
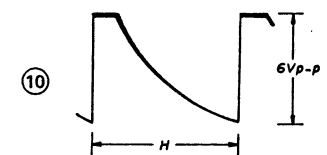
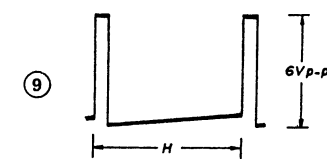
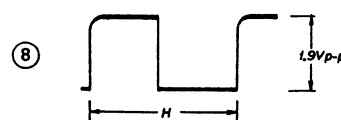
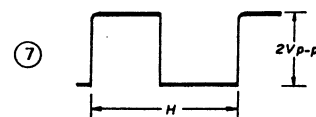
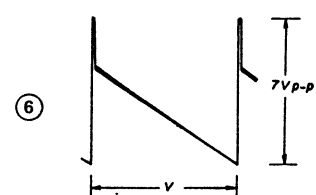
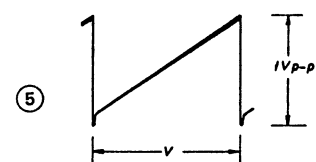
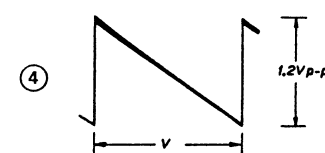
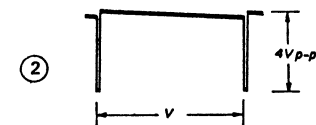
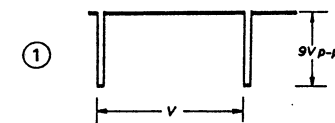


The diagram illustrates the internal circuitry of a television receiver, organized into four main functional boards:

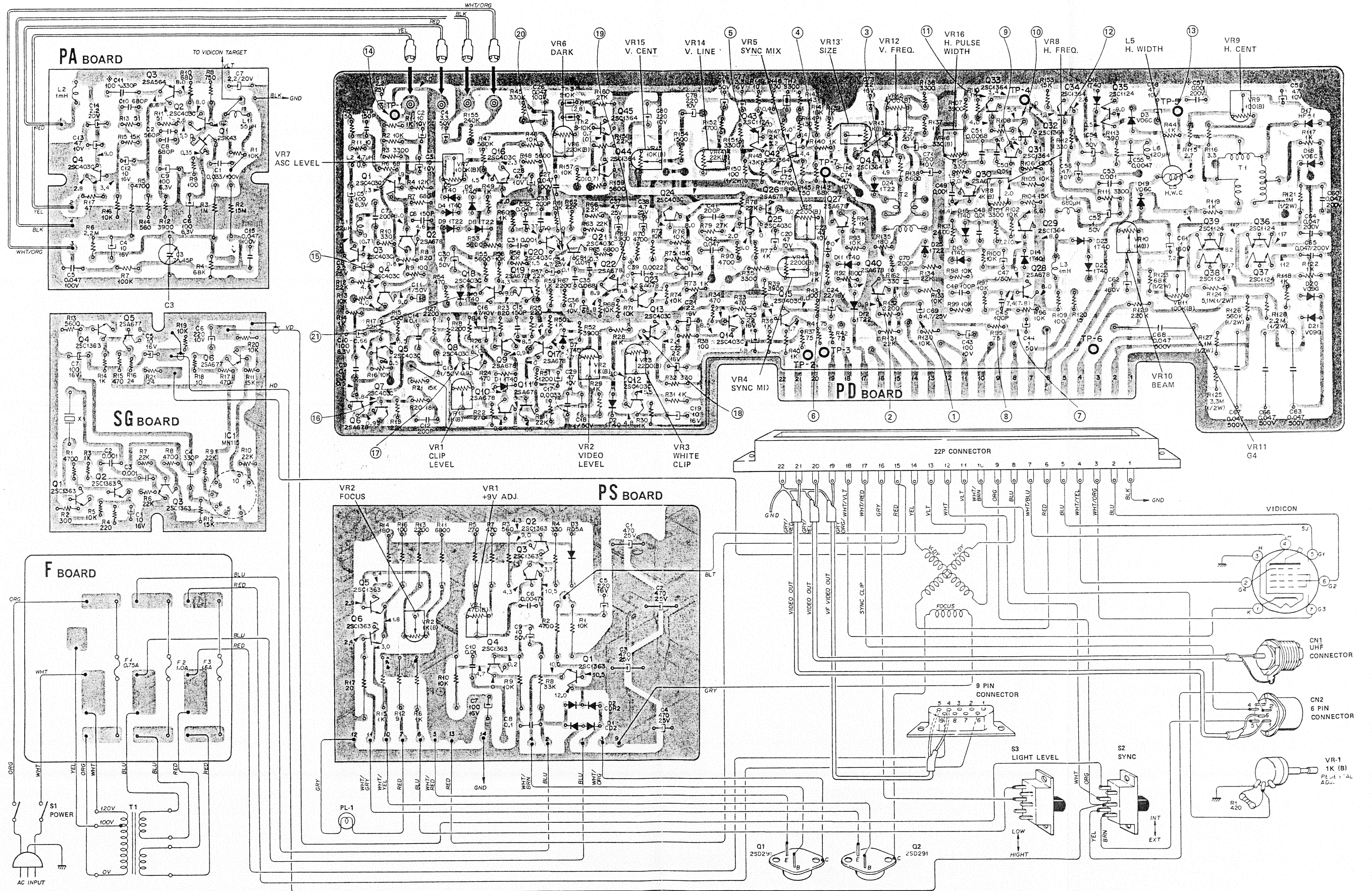
- PA BOARD (Power Amplifier Board):** Located at the top left, it features a power transformer (T1) and various components for audio amplification and output. Key components include transistors Q1, Q2, Q3, Q4, Q5, Q6, Q7, Q8, Q9, Q10, Q11, Q12, Q13, Q14, Q15, Q16, Q17, Q18, Q19, Q20, Q21, Q22, Q23, Q24, Q25, Q26, Q27, Q28, Q29, Q30, Q31, Q32, Q33, Q34, Q35, Q36, Q37, Q38, Q39, Q40, Q41, Q42, Q43, Q44, Q45, Q46, Q47, Q48, Q49, Q50, Q51, Q52, Q53, Q54, Q55, Q56, Q57, Q58, Q59, Q60, Q61, Q62, Q63, Q64, Q65, Q66, Q67, Q68, Q69, Q70, Q71, Q72, Q73, Q74, Q75, Q76, Q77, Q78, Q79, Q80, Q81, Q82, Q83, Q84, Q85, Q86, Q87, Q88, Q89, Q90, Q91, Q92, Q93, Q94, Q95, Q96, Q97, Q98, Q99, Q100, Q101, Q102, Q103, Q104, Q105, Q106, Q107, Q108, Q109, Q110, Q111, Q112, Q113, Q114, Q115, Q116, Q117, Q118, Q119, Q120, Q121, Q122, Q123, Q124, Q125, Q126, Q127, Q128, Q129, Q130, Q131, Q132, Q133, Q134, Q135, Q136, Q137, Q138, Q139, Q140, Q141, Q142, Q143, Q144, Q145, Q146, Q147, Q148, Q149, Q150, Q151, Q152, Q153, Q154, Q155, Q156, Q157, Q158, Q159, Q160, Q161, Q162, Q163, Q164, Q165, Q166, Q167, Q168, Q169, Q170, Q171, Q172, Q173, Q174, Q175, Q176, Q177, Q178, Q179, Q180, Q181, Q182, Q183, Q184, Q185, Q186, Q187, Q188, Q189, Q190, Q191, Q192, Q193, Q194, Q195, Q196, Q197, Q198, Q199, Q200, Q201, Q202, Q203, Q204, Q205, Q206, Q207, Q208, Q209, Q210, Q211, Q212, Q213, Q214, Q215, Q216, Q217, Q218, Q219, Q220, Q221, Q222, Q223, Q224, Q225, Q226, Q227, Q228, Q229, Q230, Q231, Q232, Q233, Q234, Q235, Q236, Q237, Q238, Q239, Q240, Q241, Q242, Q243, Q244, Q245, Q246, Q247, Q248, Q249, Q250, Q251, Q252, Q253, Q254, Q255, Q256, Q257, Q258, Q259, Q260, Q261, Q262, Q263, Q264, Q265, Q266, Q267, Q268, Q269, Q270, Q271, Q272, Q273, Q274, Q275, Q276, Q277, Q278, Q279, Q280, Q281, Q282, Q283, Q284, Q285, Q286, Q287, Q288, Q289, Q290, Q291, Q292, Q293, Q294, Q295, Q296, Q297, Q298, Q299, Q300, Q301, Q302, Q303, Q304, Q305, Q306, Q307, Q308, Q309, Q310, Q311, Q312, Q313, Q314, Q315, Q316, Q317, Q318, Q319, Q320, Q321, Q322, Q323, Q324, Q325, Q326, Q327, Q328, Q329, Q330, Q331, Q332, Q333, Q334, Q335, Q336, Q337, Q338, Q339, Q340, Q341, Q342, Q343, Q344, Q345, Q346, Q347, Q348, Q349, Q350, Q351, Q352, Q353, Q354, Q355, Q356, Q357, Q358, Q359, Q360, Q361, Q362, Q363, Q364, Q365, Q366, Q367, Q368, Q369, Q370, Q371, Q372, Q373, Q374, Q375, Q376, Q377, Q378, Q379, Q380, Q381, Q382, Q383, Q384, Q385, Q386, Q387, Q388, Q389, Q390, Q391, Q392, Q393, Q394, Q395, Q396, Q397, Q398, Q399, Q400, Q401, Q402, Q403, Q404, Q405, Q406, Q407, Q408, Q409, Q410, Q411, Q412, Q413, Q414, Q415, Q416, Q417, Q418, Q419, Q420, Q421, Q422, Q423, Q424, Q425, Q426, Q427, Q428, Q429, Q430, Q431, Q432, Q433, Q434, Q435, Q436, Q437, Q438, Q439, Q440, Q441, Q442, Q443, Q444, Q445, Q446, Q447, Q448, Q449, Q450, Q451, Q452, Q453, Q454, Q455, Q456, Q457, Q458, Q459, Q460, Q461, Q462, Q463, Q464, Q465, Q466, Q467, Q468, Q469, Q470, Q471, Q472, Q473, Q474, Q475, Q476, Q477, Q478, Q479, Q480, Q481, Q482, Q483, Q484, Q485, Q486, Q487, Q488, Q489, Q490, Q491, Q492, Q493, Q494, Q495, Q496, Q497, Q498, Q499, Q500, Q501, Q502, Q503, Q504, Q505, Q506, Q507, Q508, Q509, Q510, Q511, Q512, Q513, Q514, Q515, Q516, Q517, Q518, Q519, Q520, Q521, Q522, Q523, Q524, Q525, Q526, Q527, Q528, Q529, Q530, Q531, Q532, Q533, Q534, Q535, Q536, Q537, Q538, Q539, Q540, Q541, Q542, Q543, Q544, Q545, Q546, Q547, Q548, Q549, Q550, Q551, Q552, Q553, Q554, Q555, Q556, Q557, Q558, Q559, Q560, Q561, Q562, Q563, Q564, Q565, Q566, Q567, Q568, Q569, Q570, Q571, Q572, Q573, Q574, Q575, Q576, Q577, Q578, Q579, Q580, Q581, Q582, Q583, Q584, Q585, Q586, Q587, Q588, Q589, Q590, Q591, Q592, Q593, Q594, Q595, Q596, Q597, Q598, Q599, Q600, Q601, Q602, Q603, Q604, Q605, Q606, Q607, Q608, Q609, Q610, Q611, Q612, Q613, Q614, Q615, Q616, Q617, Q618, Q619, Q620, Q621, Q622, Q623, Q624, Q625, Q626, Q627, Q628, Q629, Q630, Q631, Q632, Q633, Q634, Q635, Q636, Q637, Q638, Q639, Q640, Q641, Q642, Q643, Q644, Q645, Q646, Q647, Q648, Q649, Q650, Q651, Q652, Q653, Q654, Q655, Q656, Q657, Q658, Q659, Q660, Q661, Q662, Q663, Q664, Q665, Q666, Q667, Q668, Q669, Q670, Q671, Q672, Q673, Q674, Q675, Q676, Q677, Q678, Q679, Q680, Q681, Q682, Q683, Q684, Q685, Q686, Q687, Q688, Q689, Q690, Q691, Q692, Q693, Q694, Q695, Q696, Q697, Q698, Q699, Q700, Q701, Q702, Q703, Q704, Q705, Q706, Q707, Q708, Q709, Q710, Q711, Q712, Q713, Q714, Q715, Q716, Q717, Q718, Q719, Q720, Q721, Q722, Q723, Q724, Q725, Q726, Q727, Q728, Q729, Q730, Q731, Q732, Q733, Q734, Q735, Q736, Q737, Q738, Q739, Q740, Q741, Q742, Q743, Q744, Q745, Q746, Q747, Q748, Q749, Q750, Q751, Q752, Q753, Q754, Q755, Q756, Q757, Q758, Q759, Q760, Q761, Q762, Q763, Q764, Q765, Q766, Q767, Q768, Q769, Q770, Q771, Q772, Q773, Q774, Q775, Q776, Q777, Q778, Q779, Q780, Q781, Q782, Q783, Q784, Q785, Q786, Q787, Q788, Q789, Q790, Q791, Q792, Q793, Q794, Q795, Q796, Q797, Q798, Q799, Q800, Q801, Q802, Q803, Q804, Q805, Q8

AVC-3260 SCHEMATIC DIAGRAM





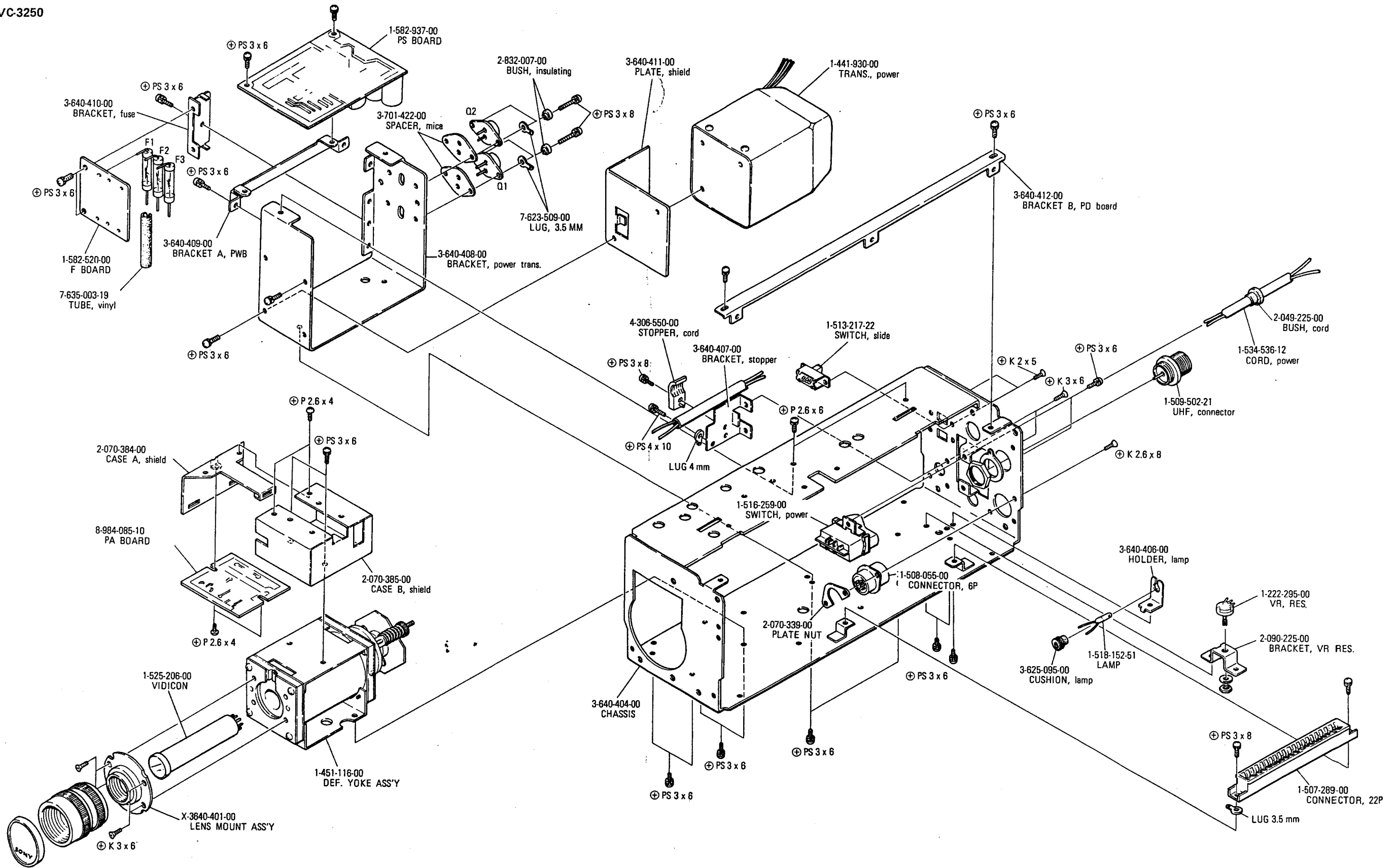
AVC-3260 MOUNTING DIAGRAM



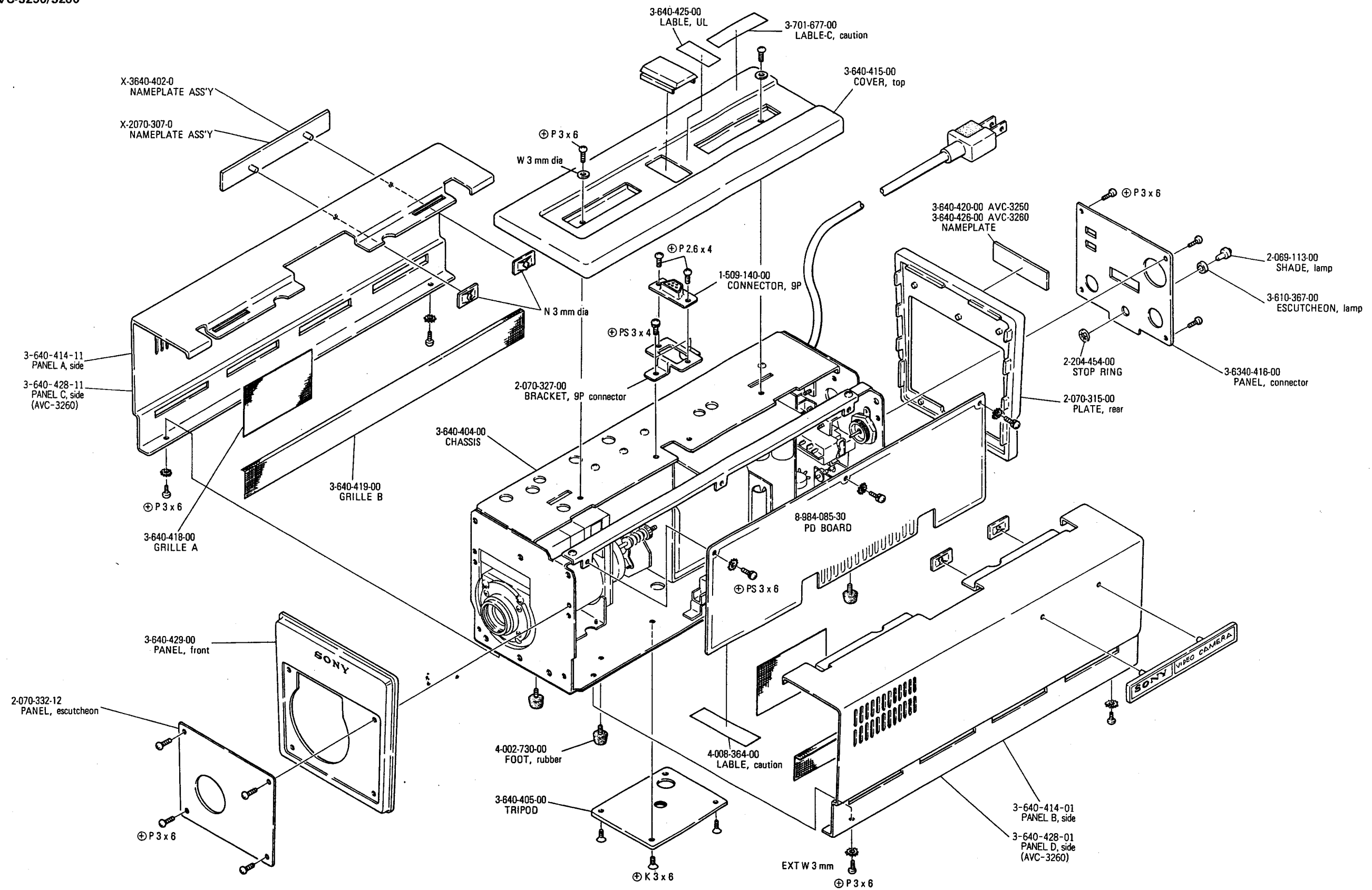
SECTION 7

EXPLODED VIEW WITH PARTS NUMBER

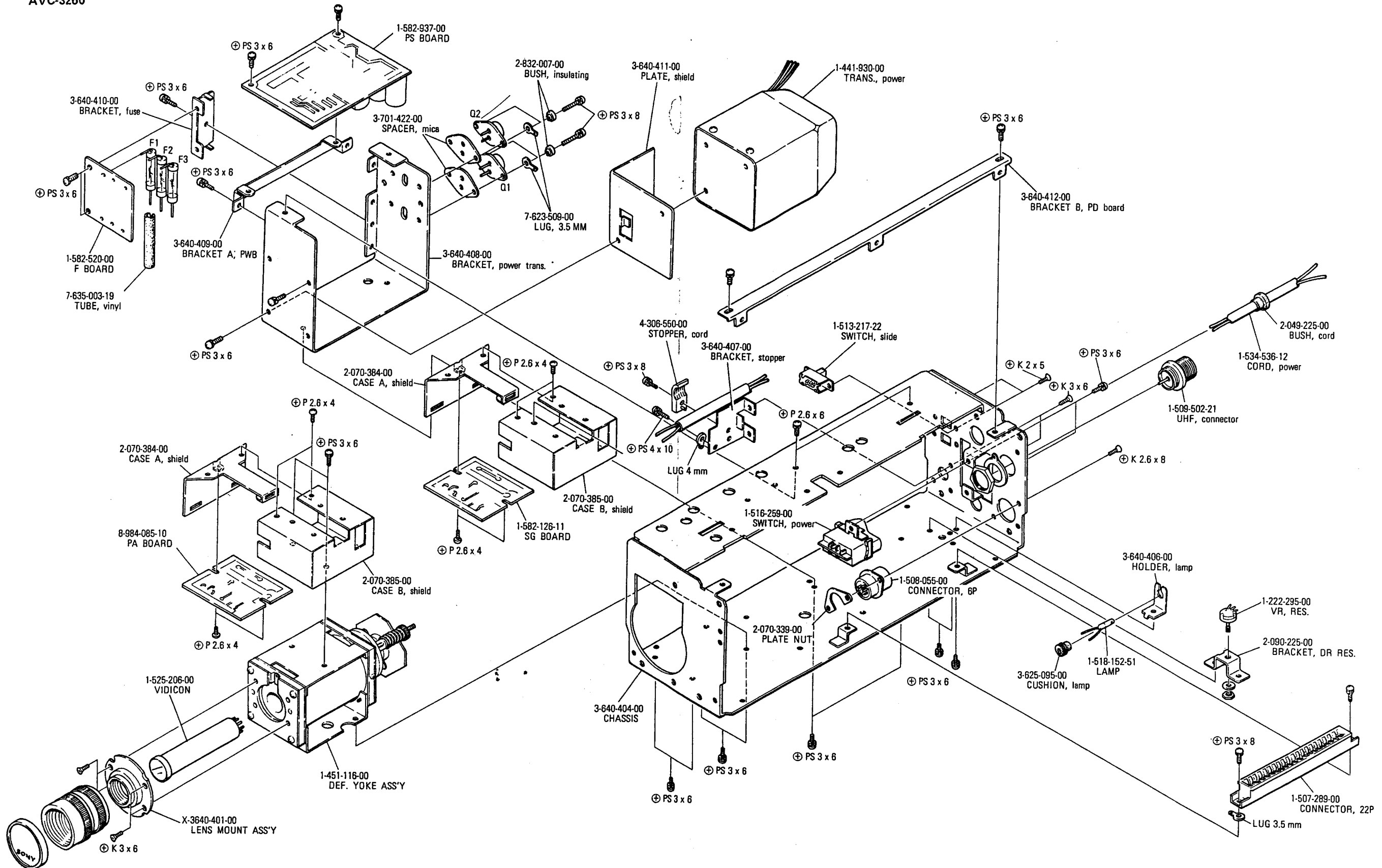
AVC-3250



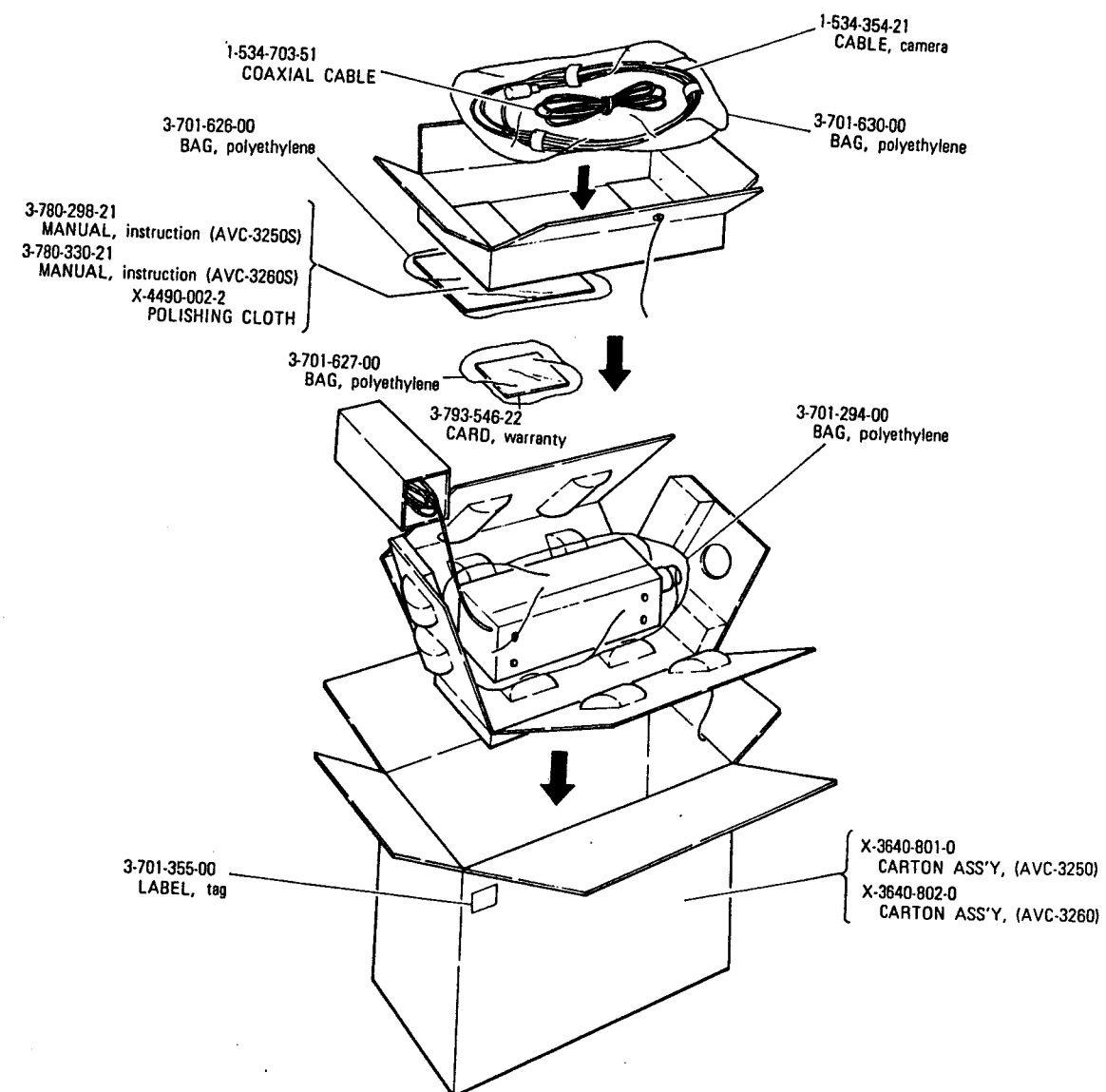
AVC-3250/3260



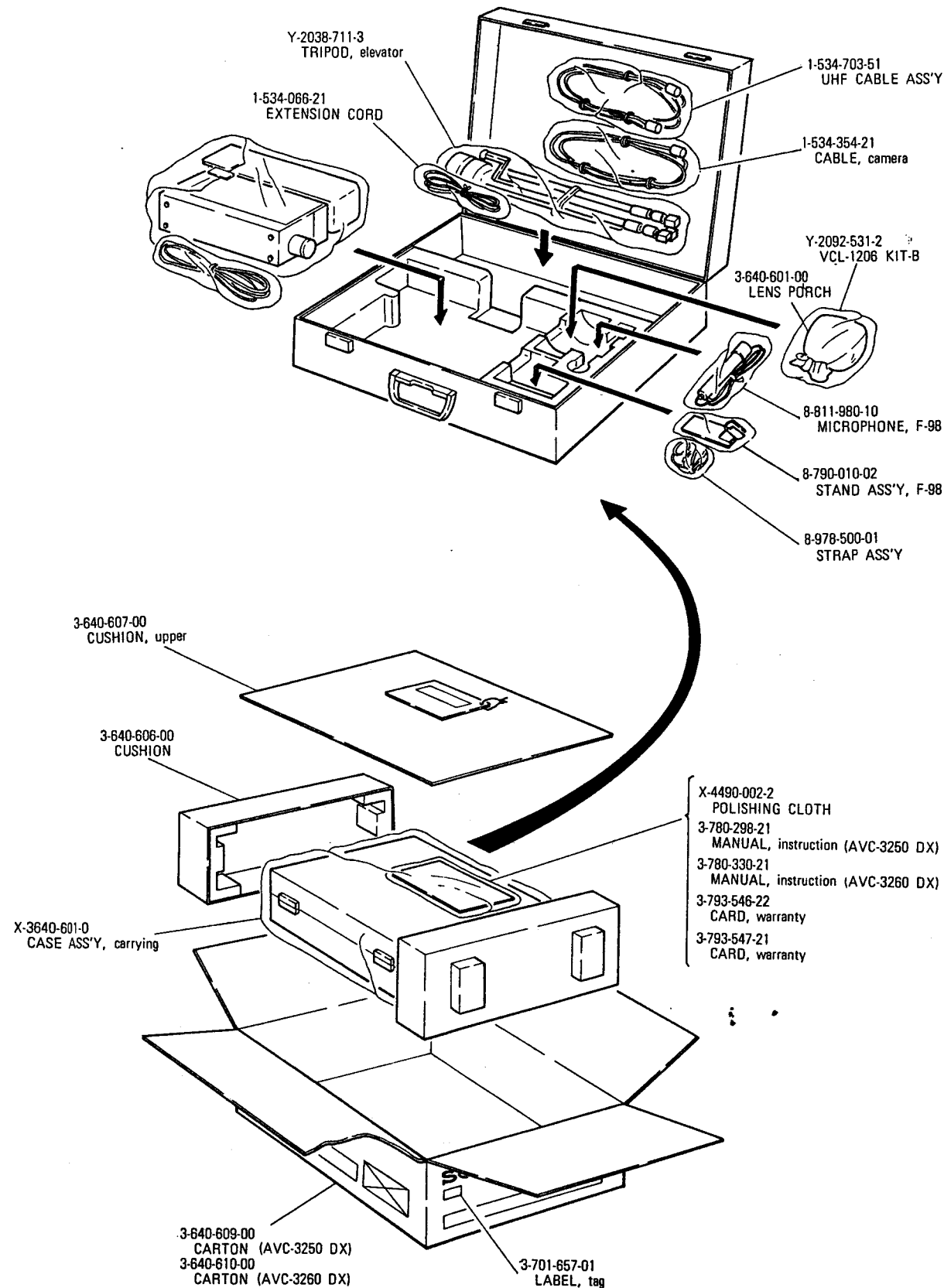
AVC-3260



PACKING



SECTION 8
ELECTRICAL PARTS LIST



Ref. No. Part No. Description

PA BOARD

8-984-085-10 PA MTD Board

Semiconductors

Q1 2SK43
Q1,4 2SC403B
Q3 2SA564A

Capacitors

C8,10 1-102-116-11 Ceramic 680 pF 50V
C1 1-105-719-12 Mylar 0.033 μ F 100V
C5,15 1-105-721-12 Mylar 0.047 μ F 100V
C4,12 1-121-651-11 Elect 10 μ F 16V
C13 1-121-352-11 Elect 47 μ F 10V

C6,9 1-121-413-11 Elect 100 μ F 6.3V
C7,14 1-131-196-11 Tantal 2.2 μ F 20V
C2 1-102-115-11 Ceramic 560 pF 50V
C11 1-107-085-11 Mica 100 pF 50V
C11 1-107-091-11 Mica 180 pF 50V

C11 1-107-095-11 Mica 270 pF 50V
C11 1-107-097-11 Mica 330 pF 50V
C3 1-141-074-11 Trimmer 7~45 pF

Inductors

L2 1-407-195-21 Inductor 1 mH
L1 1-407-787-11 Inductor 55 μ H

Resistors

Carbon resistors in ohm, \pm 5%, 1/4W are omitted.

PS BOARD

8-984-085-00 PS MTD Board

Semiconductor

Q1~6 2SC1363
D3 RD5A
D1,2 CD-2

Capacitors

C6 1-105-669-12 Mylar 0.0047 μ F 50V
C10 1-105-673-12 Mylar 0.01 μ F 50V
C8 1-105-685-12 Mylar 0.1 μ F 50V
C9 1-121-738-11 Elect 10 μ F 50V
C7 1-121-415-11 Elect 100 μ F 16V

Ref. No. Part No. Description

C5 1-121-421-11 Elect 220 μ F 16V
C1~4 1-121-733-11 Elect 470 μ F 25V

Resistors

Carbon resistors in ohm, \pm 5%, 1/4W are omitted

R17 1-213-167-11 Metal 20 1/2W

VR-1 1-222-804-11 VR RES 1K-B
VR-2 1-222-805-11 VR RES 470-B

1-582-520-11 F Board (USA, CND)
F1 1-532-264-11 0.75 A (USA, CND)
F2 1-532-265-11 1 A (USA, CND)
F3 1-532-267-11 1.6 A (USA, CND)

PD BOARD

8-984-085-70 PD MTD Board

Semiconductors

Q1,3~5,7,8,12~16, 2SC403C
18,20,21,24,25
Q35~39,43 2SC1124
Q2,6,9~11,17,19,22, 2SC678
23,26~28,30,40
Q29,31~34 2SC1364
Q41,42,44,45
D17 HF-1
D1,2,4~7,10,11 IT40
13~16,22,23
D8,9,12 IT22A
24,25
D3,18,19 V06C
D20,21 V09G

Capacitors

*39,C31, 1-105-661-12 Mylar 0.001 μ F 50V
49,53
*39,C17 1-105-667-12 Mylar 0.0033 μ F 50V
C55 1-105-669-12 Mylar 0.0047 μ F 50V
C48,50, 1-105-673-12 Mylar 0.01 μ F 50V
81
C32,42 1-105-681-12 Mylar 0.047 μ F 50V
C9,14, 1-105-685-12 Mylar 0.1 μ F 50V
40
C64,65 1-105-761-12 Mylar 0.047 μ F 200V
C26,27 1-105-721-12 Mylar 0.047 μ F 100V
C45,46 1-107-085-11 Mica 100 pF 50V
C6,15, 1-107-089-11 Mica 150 pF 50V
79

<u>Ref. No.</u>	<u>Part No.</u>	<u>Description</u>		
C7,12,33 41,70	1-107-092-11	Mica	200 pF	50V
C63, 66~68	1-113-122-11	Paper	0.047 μ F	500V
C61,62	1-121-189-11	Elect	1 μ F	160V
C11,13,18 44,47,52	1-121-391-11	Elect	1 μ F	50V
54,76 C4	1-121-393-11	Elect	3.3 μ F	50V
C34,37, 38,69	1-121-395-11	Elect	4.7 μ F	25V
C2,3,19	1-121-651-11	Elect	10 μ F	16V
C16,20,23 74,75,28, 29,36,56, 58,59	1-121-352-11	Elect	47 μ F	10V
C30	1-121-411-11	Elect	47 μ F	50V
C5,10	1-121-413-11	Elect	100 μ F	6.3V
C43,71	1-107-414-11	Elect	100 μ F	10V
C78,80	1-121-420-11	Elect	220 μ F	10V
C1,8,12	1-131-205-11	Tantal	2.2 μ F	25V
C77	1-131-197-11	Tantal	3.3 μ F	16V
C25,73, 21,22,24	1-131-201-11	Tantal	22 μ F	16V
*C39	1-131-665-12	Mylar	0.0022 μ F	50V
*C51	1-131-670-12	Mylar	0.0056 μ F	50V
*C51	1-131-671-12	Mylar	0.0068 μ F	50V
C51	1-105-672-12	Mylar	0.0082 μ F	50V
*C57	1-105-741-12	Mylar	0.001 μ F	200V
C57	1-106-743-12	Mylar	0.0015 μ F	200V
C57	1-105-745-12	Mylar	0.0022 μ F	200V
Inductors				
L1	1-407-161-11	Inductor	22 μ H	
L6	1-407-170-11	Inductor	120 μ H	
L2	1-407-186-11	Inductor	4.7 μ H	
L4	1-407-193-11	Inductor	680 μ H	
L3	1-407-195-11	Inductor	1 μ H	
L5	1-407-352-11	Coil, H size		
Resistors				
Carbon resistors in ohm, \pm 5%, 1/4W are omitted.				
R123	1-202-617-11	COMPO 68 k	1/2W	
R126	1-202-639-11	COMPO 560 k	1/2W	
R121	1-202-645-11	COMPO 1M		
R128	1-202-653-11	COMPO 2.2M		
R125	1-202-657-11	COMPO 3.3M		
R124, 127	1-202-662-11	COMPO 5.1M	1/2W	
VR9	1-224-247-11	VR RES 100-B		
VR12	1-221-978-11	VR RES 4.7K-B		
VR14	1-221-979-11	VR RES 22K-B		
VR16	1-221-986-11	VR RES 330-B		

<u>Ref. No.</u>	<u>Part No.</u>	<u>Description</u>		
VR3,4,5	1-121-997-11	VR RES 2.2K-B		
VR15	1-222-701-11	VR RES 10K-B		
VR1,2,8 13	1-222-804-11	VR RES 1K-B		
VR7,11	1-222-845-11	VR RES 100K-B		
VR10	1-222-846-11	VR RES 1M-B		
VR6	1-222-894-11	VR RES 220K-B		
Transformers				
T1	1-439-151-11	FBT		
T2	1-435-008-21	VBT		
Thermistors				
TH1	1-800-196-11	S-300		
TH2,3	1-800-202-11	S-10K		
Others				
	1-506-108-11	SV Terminal		
SG BOARD				
(AVC-3260 Exclusively)				
Semiconductors				
Q5,6		2SA678		
Q1~4		2SC1364		
IC1		MN-115		
Capacitors				
C2,3	1-105-661-12	Mylar	0.001 μ F	50V
C4	1-107-097-11	Mica	330 pF	50V
C1,5	1-121-471-11	Elect	10 μ F	16V
C7	1-121-415-11	Elect	100 μ F	16V
C6	1-121-420-11	Elect	220 μ F	16V
Resistors				
Carbon resistors in ohm, \pm 5%, 1/4 are omitted.				
Crystal				
X1	1-527-238-11	31.468 KHz		
CHASSIS				
Semiconductor				
Q1,2		2SD292		

<u>Ref. No.</u>	<u>Part No.</u>	<u>Description</u>		
Capacitor				
C1	1-113-122-11	Paper	0.047 μ F	500V
Resistors				
Carbon resistors in ohm, $\pm 5\%$, 1/4 are omitted.				
VR1	1-222-295-11	VR RES	1K-ohm-B	
Transformer				
T1	1-441-930-11	Power (USA, CND)		

<u>Ref. No.</u>	<u>Part No.</u>	<u>Description</u>	
		Others	
CN4	1-507-289-11	Connector,	22 P
CN2	1-508-055-11	Connector	6 P
CN3	1-509-140-12	Connector	9 P
CN1	1-509-502-21	Connector,	UHF
S2,3	1-513-217-22	Switch, slide	
S1	1-516-259-11	Switch, power	
PL1	1-518-152-51	LAMP	
	1-525-206-00	VIDICON	
	1-451-116-11	DEF. YOKE ASY	
	1-534-536-12	Cord, power (USA, CND)	
	1-536-047-11	Terminal	